



03/2019



**Barcelona
Supercomputing
Center**
Centro Nacional de Supercomputación



EXCELENCIA
SEVERO
OCHOA

BSC y lo que la supercomputación puede hacer por nosotros

Prof. Mateo Valero
BSC Director



X ANIVERSARIO DE
CénitS - COMPUTAEX
10 Años de Supercomputación en Extremadura

Mi vida como investigador

Prof. Mateo Valero, UPC, Barcelona

Master en Gestió I Política Universitaria
Barcelona 5 de Julio de 2004

Historia

- Nacido en Alfamén, Zaragoza, 6 de Agosto de 1952
- Escuela en Alfamén, 1956-1961
- Bachillerato: Padres Escolapios, Zaragoza, 1961-1968
- Ingeniería de Telecomunicación:
 - Universidad de Zaragoza, 1969-1970
 - ETSIT, Madrid 1970-1974
- Profesor de la ETSIT de Barcelona, UPC, 1974- ahora
 - Tesis: 1974-1980
 - Mucha dificultad
- Profesor de la Facultad de Informática, UPC, 1980-ahora
 - Crear departamento: asignaturas, contratar,..
 - Empezar a investigar
 - Situación española... no hay \$, no existe nada,... CICYT
- Decisiones Estratégicas:
 - Arquitectura de Computadores
 - Supercomputadores... CEPBA, C4, CIRI y BSC

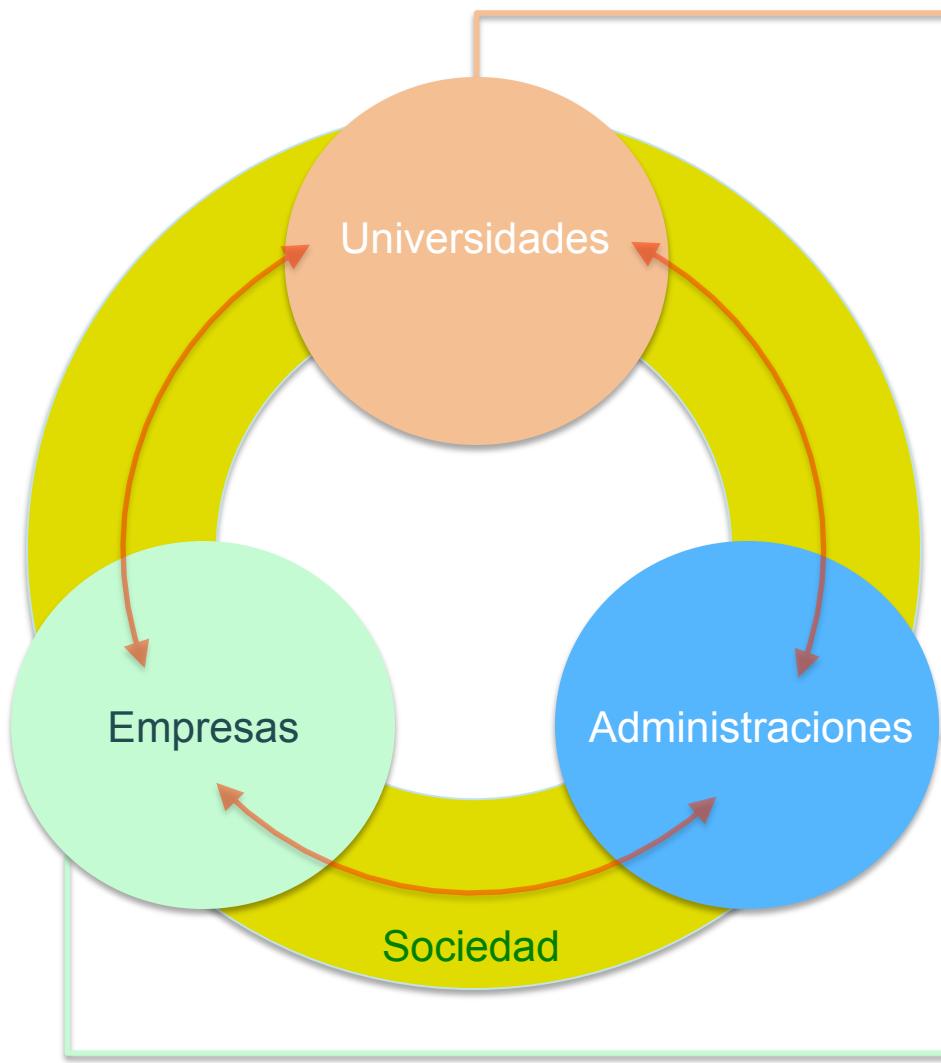








Ecosistema de la investigación



- Hemos avanzado mucho (CE)
- Formar buenos investigadores
- Investigar, obligación del profesorado
- Financiación = ϕ (investigación)
- Internacionalización (EU, máster, PhD)
- Atraer capital humano
- Atraer/crear empresas

- Reconocimiento
recursos → investigación → **recursos**
- Países ricos/pobres
- Constancia en los recursos
- Gestión eficiente de recursos
- Simplificar/facilitar procesos
- Pacto de Estado para la Ciencia

- Gran avance colaboración Universidad/Empresa
- Receptora de doctores (publicaciones/ patentes)
- Financiación de la investigación
- Incentivos fiscales
- Capital Riesgo

Investigar es

- Cumplir con una obligación laboral
- Mejorar la docencia
- Promoción
- Ser independiente
- Un lujo al alcance de pocos
- Producir riqueza
- Estar enamorado de tu trabajo

Mateo Valero: Mi vida como investigador, 2004

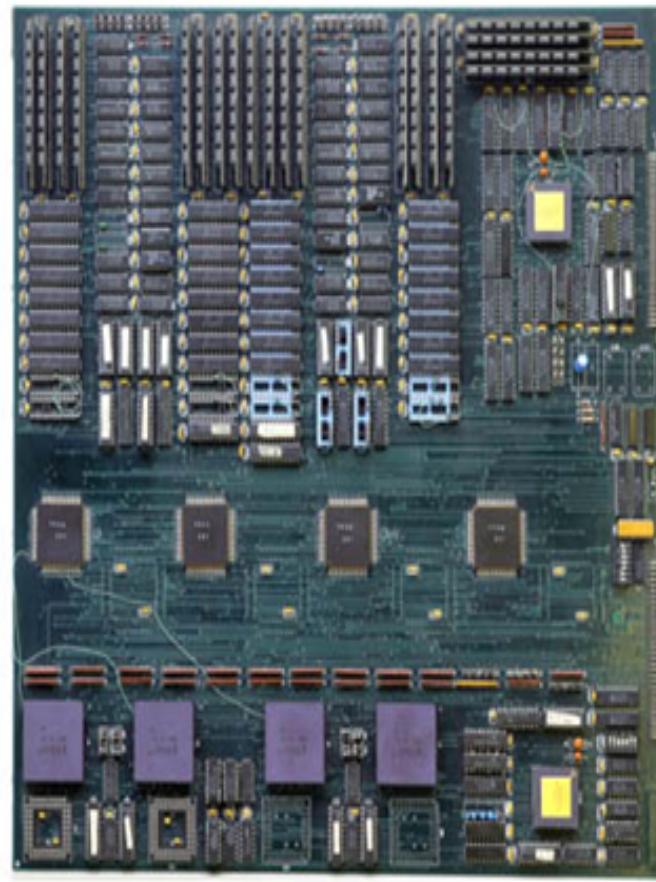
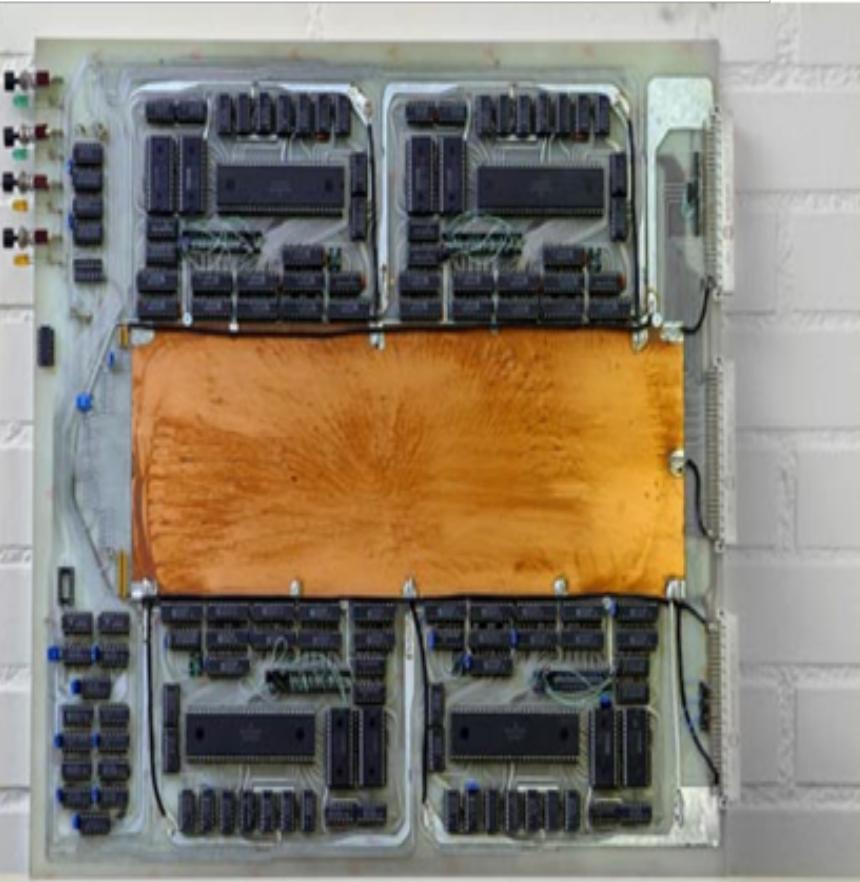
Conceptos Básicos sobre Investigación

- Investigación Básica/Fundamental versus Investigación Aplicada
- Investigación Buena versus Mala
- La buena investigación produce siempre riqueza
 - Investigación a corto/medio y largo plazo
- Resultados de una buena investigación:
 - Artículos y patentes
 - Gente formada
- Una buena formación académica es fundamental
- La cooperación entre los grupos de investigación y las empresas son la otra parte de la película
- Hacer una **buenas investigación** es el único camino que tenemos en Europa para ser competitivos en un futuro a medio/largo plazo

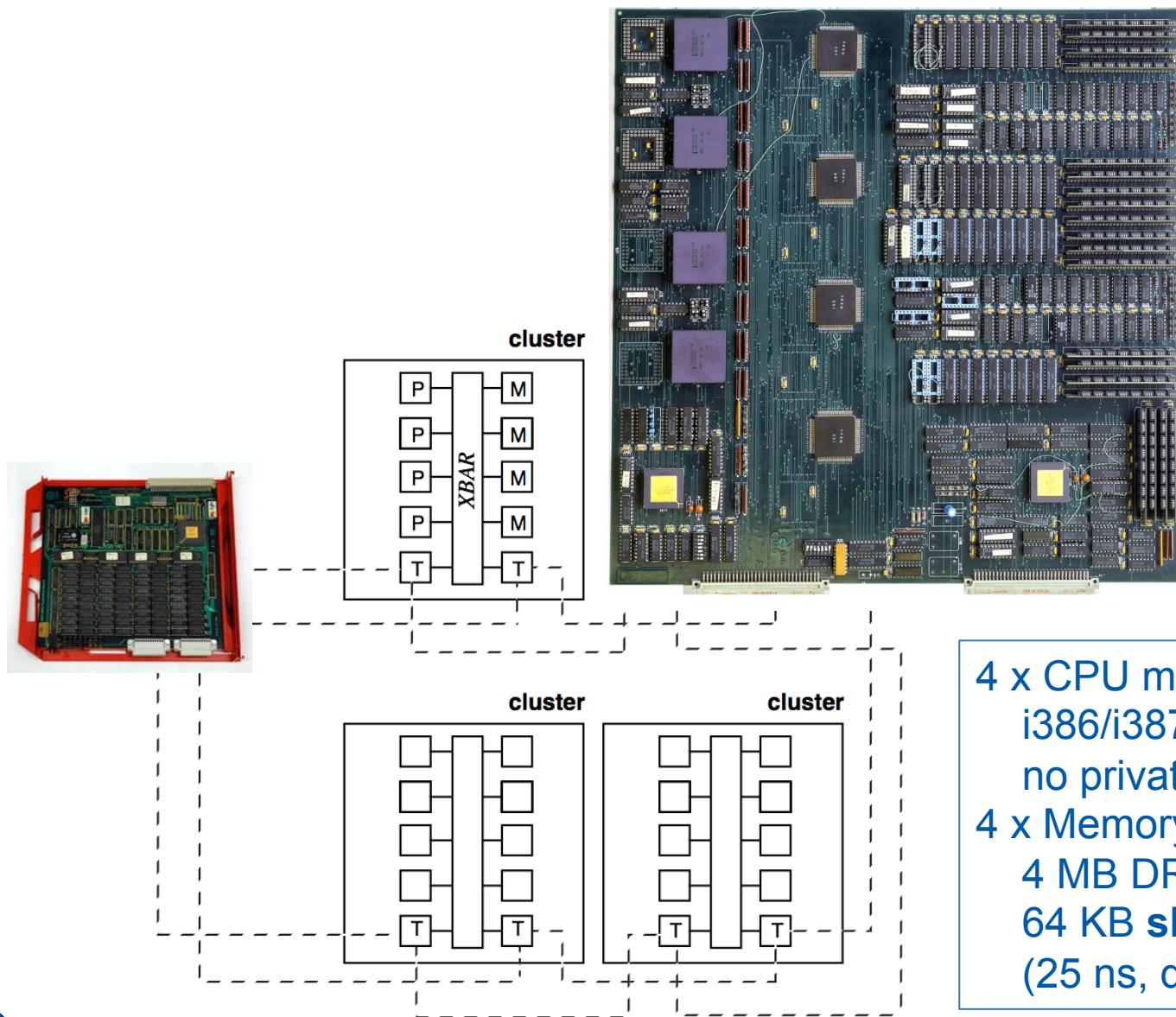
Mateo Valero: Mi vida como investigador, 2004

Once upon a time ...

1986 and 1988 , UPC multiprocessor prototypes



ICT386 multiprocessor architecture



4 x CPU module
i386/i387
no private cache memory
4 x Memory module
4 MB DRAM (100 ns)
64 KB **shared cache**
(25 ns, direct-mapped)

Professor Tomas Lang



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Our Origins.....Plan Nacional de Investigación

High-performance Computing group @ Computer Architecture Department (UPC)



Barcelona Systems and Servers
Advanced Development



IBM Innovation Initiative at BSC
Technology Center for Supercomputing
IBM-BSC Deep Learning Center



Intel and BSC Exascale Lab



Intel Labs Barcelona



BSC-Microsoft Research Centre



Relevance

Plan Nacional de i+D+i

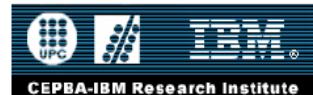
High-speed Low-cost Parallel Architecture Design		Parallelism Exploitation in High Speed Architectures		Architectures and Compilers for Supercomputers		High Performance Computing		High Performance Computing II		High Performance Computing III		High Performance Computing IV		High Performance Computing V		High Performance Computing VI		High Performance Computing VII														
PA85-0314		TIC89-299		TIC92-880		TIC95-429		TIC98-511-C02-01		TIC2001-995-C02-01		TIN2004-07739-C02-01		TIN2007-60625		TIN2012-34557		TIN2015-65316-P														
1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019

Excellence



erc
RoMoL (2013-18)
Hi_EST (2014-19)
Hi-OMICS (2017-19)
SuPerCom (2018-23)

Relevance



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Technological Achievements

« Transistor (Bell Labs,

1947)

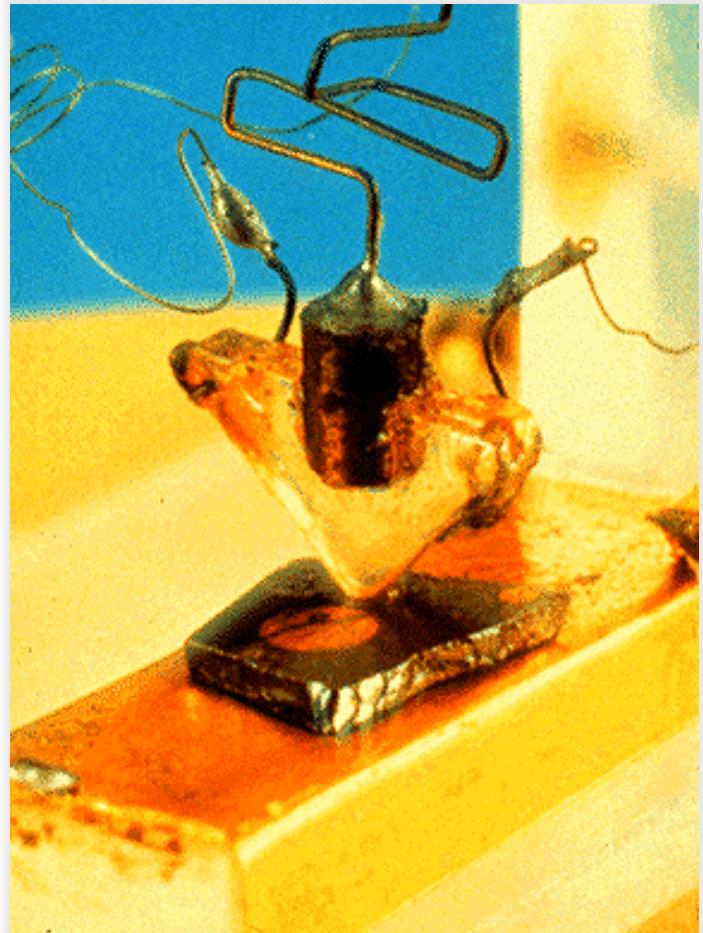
- DEC PDP-1 (1957)
- IBM 7090 (1960)

« Integrated circuit

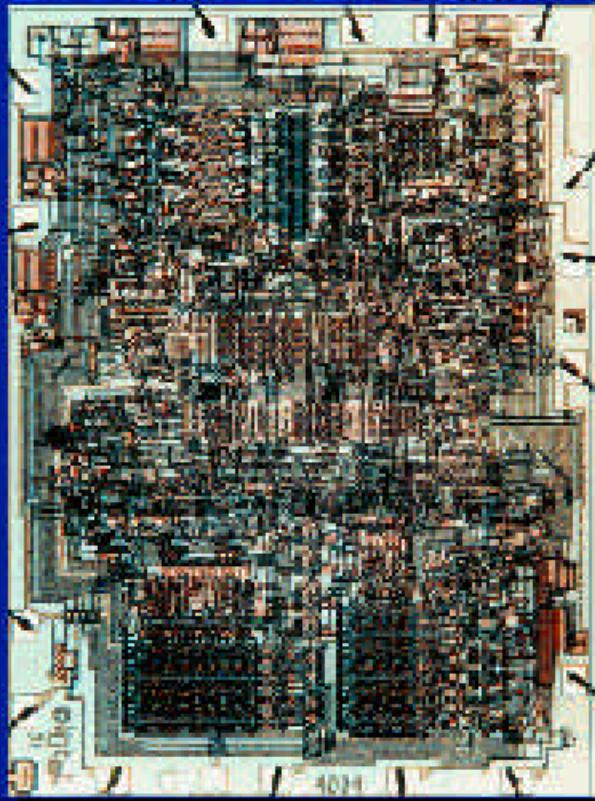
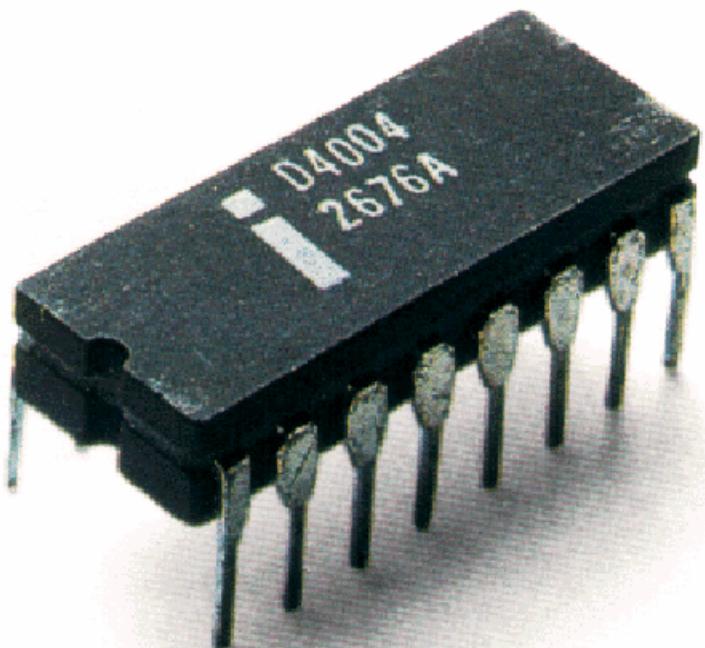
(1958)

- IBM System 360 (1965)

DEC PDP-8 (1965)



Birth of the Revolution -- The Intel 4004



Introduced November 15, 1971

108 KHz, 50 KIPs , 2300 10 μ transistors

ANNOUNCING TESLA V100

GIANT LEAP FOR AI & HPC
VOLTA WITH NEW TENSOR CORE

21B xtors | TSMC 12nm FFN | 815mm²

5,120 CUDA cores

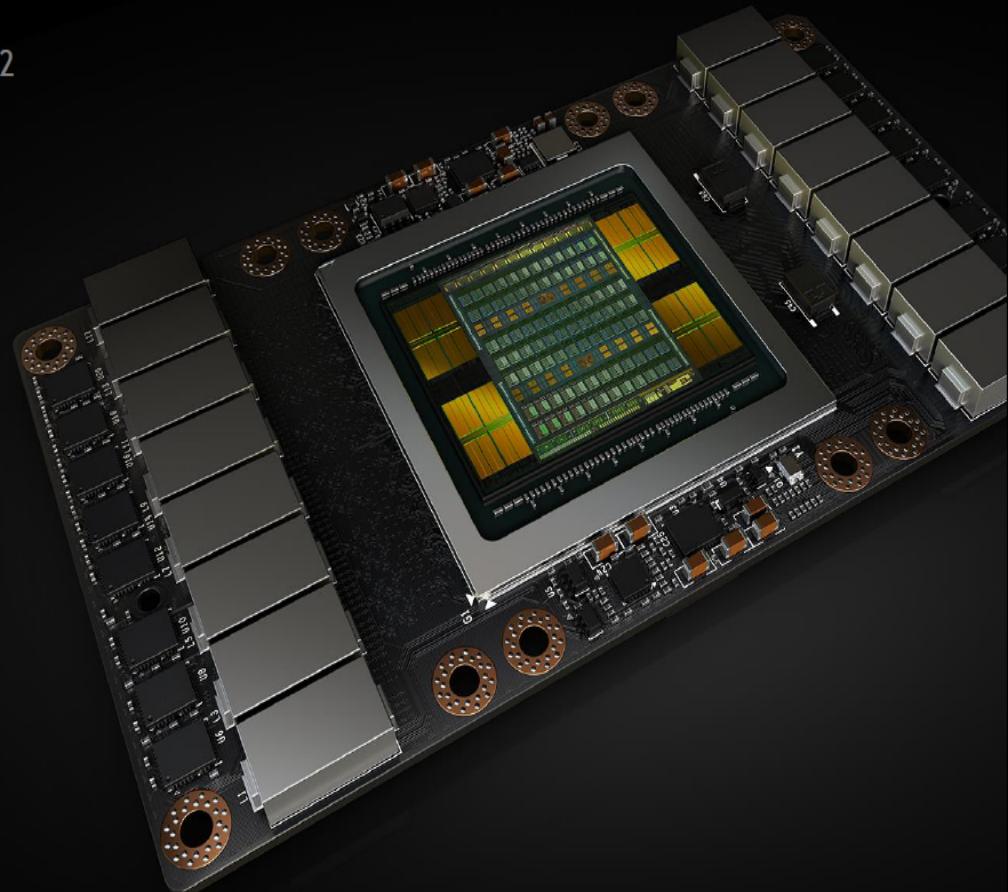
7.5 FP64 TFLOPS | 15 FP32 TFLOPS

NEW 120 Tensor TFLOPS

20MB SM RF | 16MB Cache

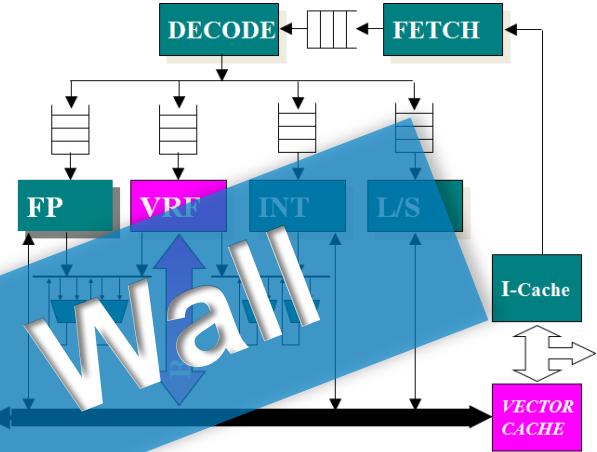
16GB HBM2 @ 900 GB/s

300 GB/s NVLink

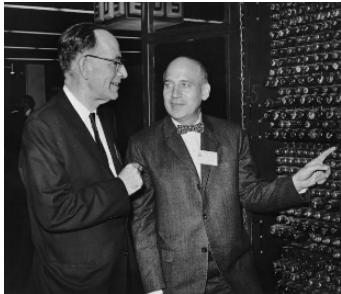


Vector Architectures... Memory Latency and Power ☺☺☺

- Out-of-Order Access to Vectors (ISCA 1992, ISCA 1995)
- Command Memory Vector (PACT 1998)
 - In-memory computation
- Decoupling Vector Architectures (HPCA 1996)
 - Cray SX1
- Out-of-order Vector Architectures (Micro 1996)
- Multithreaded Vector Architectures (HPCA 1997)
- SMT Vector Architectures (HICS 1997, IEEE MICRO 1998)
- Vector register-file organization (PACT 1997)
- Vector Microprocessors (MIC 1999, SPAA 2001)
- Architectures with Short Vectors (PACT 1997, ICS 1998)
 - Tukwila (ISA 2002), Knights Corner
- Vector Architectures for Multimedia (HPCA 2001, Micro 2002)
- High-Speed Buffers Routers (Micro 2003, IEEE TC 2006)
- Vector Architectures for Data-Base (Micro 2012, HPCA2015, ISCA2016)



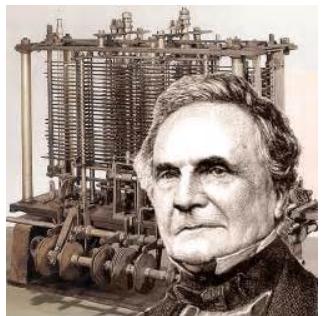
Awards in Computer Architecture



Eckert-Mauchly: IEEE Computer Society and ACM:..... “For extraordinary leadership in building a world class computer architecture research center, for seminal contributions in the areas of vector computing and multithreading, and for pioneering basic new approaches to instruction-level parallelism.” June 2007



Seymour Cray: IEEE Computer Society:..... “In recognition of seminal contributions to vector, out-of-order, multithreaded, and VLIW architectures.” November 2015



Charles Babbage: IEEE Computer Society:“For contributions to parallel computation through brilliant technical work, mentoring PhD students, and building an incredibly productive European research environment.”. April, 2017

Eckert-Mauchly Award 2007



Seymour Cray 2015





Black Outlined Butterflies
program just starting their
life's end, but on the other side of our
series, experience and talent.

Sherri Fawcett Sullens,
is an experienced quilter,
and "Quilts Around
"08 ICYMI Awards
with published by the
currently has a Microtex
Museum in Paducah,

MATEO VALERO
2015

National Research Awards

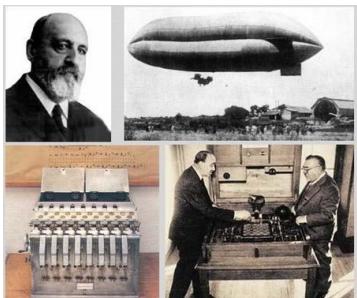


Rey Jaime I Award in Basic Research: is given by the "Generalitat de Valencia" and was presented by the Queen of Spain. This is one of the most prestigious awards in Spain. Several Nobel Prizes Laureates are members of the Jury. November 1997.



D. JULIO REY PASTOR
Estadístico de análisis matemático de la Universidad Central, que ha obtenido el premio de 12.000 pesetas en el concurso abierto por la Real Academia de Ciencias para recompensar inventos que no se reservan a medios de difusión

Julio Rey Pastor Award :This is the first Spanish National Award to recognize research in Mathematics and/or IT Technologies. The Award is given by the Spanish Ministry of Science and Technology, and was presented by the King of Spain. October, 2001



Leonardo Torres Quevedo Award:that recognizes the engineering research in the field of Science and Technology, and was presented by the king of Spain. January 2007.

Advanced ERC - Riding on Moore's Law



European Research Council

Established by the European Commission

- « 5-year ERC Advanced Grant
- « Idea: a radically new conception of parallel architectures, built using a higher level of abstraction
- « Objective: ensure continued performance improvements by riding on Moore's Law.

- « Holistic approach with parallel architecture partially implemented as a software runtime management layer
- « Multicore architecture with vector accelerators exploiting both thread and data level parallelism to optimize data movement
- « Handling parallelism, the memory wall and the power wall, in application domains from mobile to supercomputers .

Centro Europeo de Paralelismo de Barcelona

CEPBA

Technology Transfer

User Support

Research

Education

Training

HPC Facilities

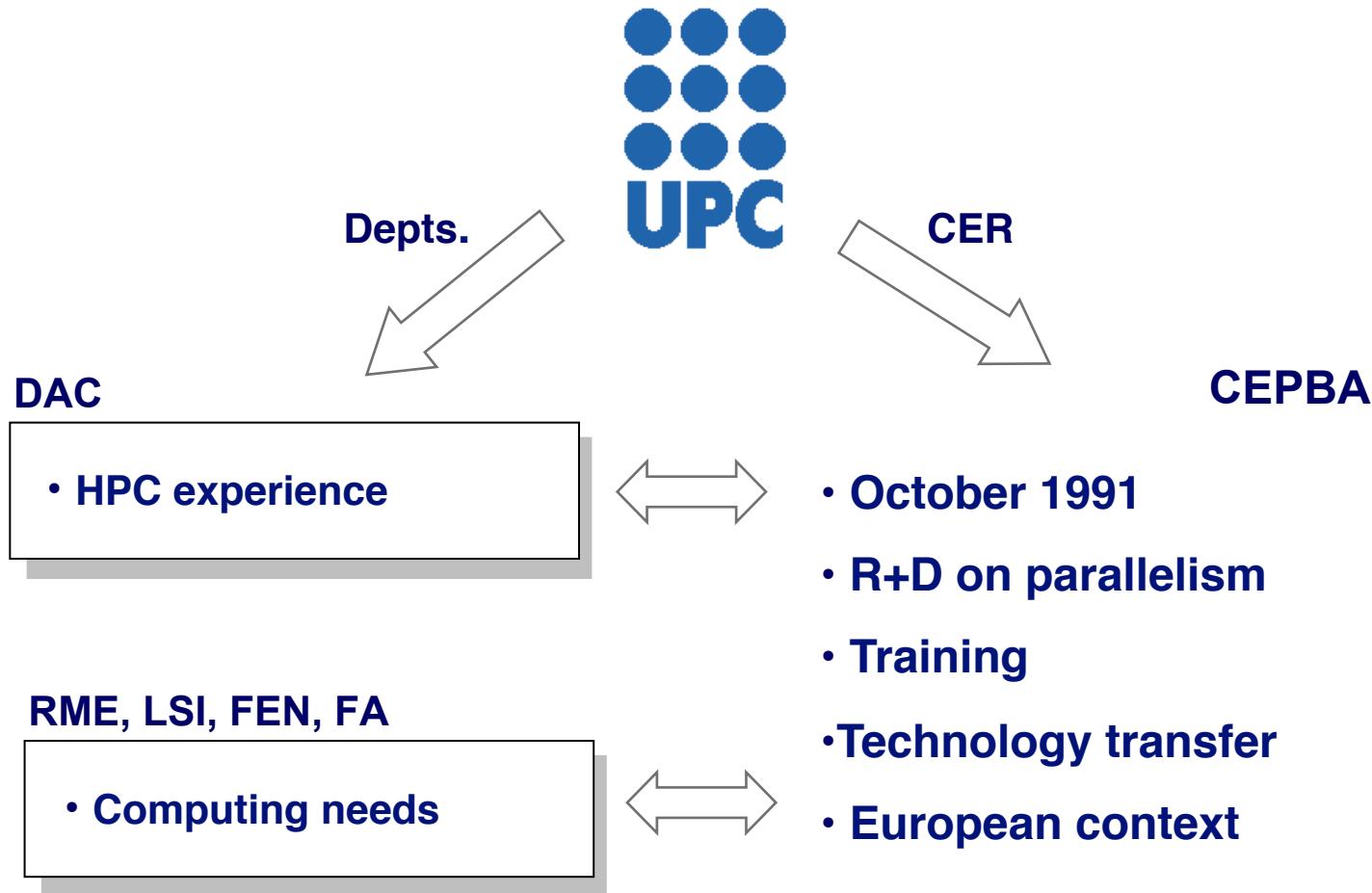
Mobility of Researchers

Parallel Expertise



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CEPBA

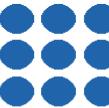


CEPBA inauguración



Venimos de muy lejos ...

Aquellos Chalados
en sus
Locos Cacharras



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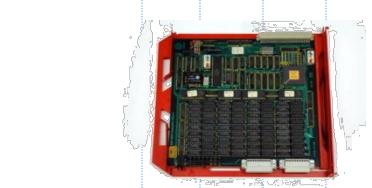
Maricel
14.4 Tflops, 20 KW



SL8500
6 Petabytes



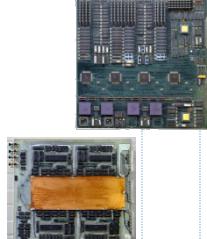
IBM PP970 / Myrinet
MareNostrum
42.35, 94.21 Tflop/s



Transputer cluster



Convex C3800



Research prototypes



Connection Machine CM-200
0.64 Gflop/s



Parsytec CCI-8D
4.45 Gflop/s



Compaq GS-140
12.5 Gflop/s



Compaq GS-160
23.4 Gflop/s



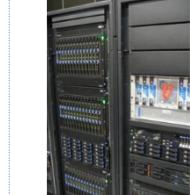
BULL NovaScale 5160
48 Gflop/s



SGI Origin 2000
32 Gflop/s



IBM RS-6000 SP & IBM p630
192+144 Gflop/s



Maricel
14.4 Tflops, 20 KW

1985 1986 1987 1988 1989 1990 1991 1992 1993 1994 1995 1996 1997 1998 1999 2000 2001 2002 2003 2004 2005 2006 2007 2008 2009 2010

Venimos de muy lejos ...

Ayto. Barcelona
Uitesa
UPC-EIO



AMES, CIMNE



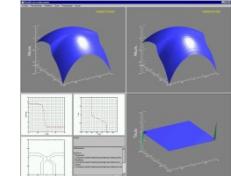
TGI
UPM-DATSI



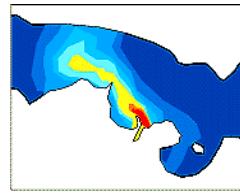
Hesperia
Neosystem
UPC-EIO



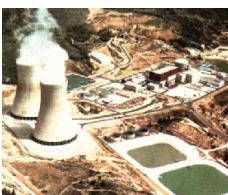
INDO, CEPBA-UPC



Metodos Cuantitativos
Gonfiesa
CESCA, CESGA



Iberdrola, Uitesa, UPV



AZTI
UPC-LIM

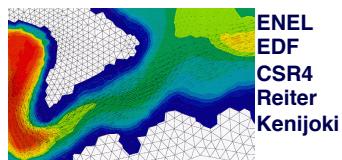
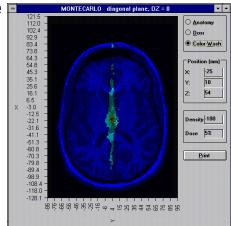


Soler y Palau
CIMNE
CEPBA-UPC



CEPBA
CESCA
UMA
UNICAN
UPM

Ospedali Galliera
Le Molinette
Parsytec
PAC
EDS



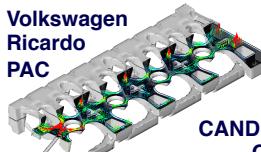
ENEL
EDF
CSR4
Reiter
Kenjoki



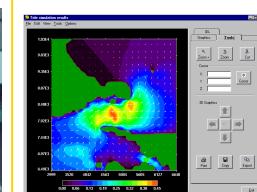
Italeco
Geospace
Intecs
Univ. Leiden



Intera SP
Intera UK
UPC-DIT
CEPBA-UPC



CANDEMAT
CIMNE
CEPBA-UPC



ST Mecanica
DERBI
AUSA
CEPBA-UPC

SENER
CIC
UNICAN

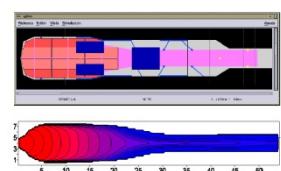


CEBAL-ENTEC
NEOSYSTEMS



Iberdrola
SAGE
CEPBA-UPC

Cari Verona
AIS
PAC
Univ.
Cat. Milan

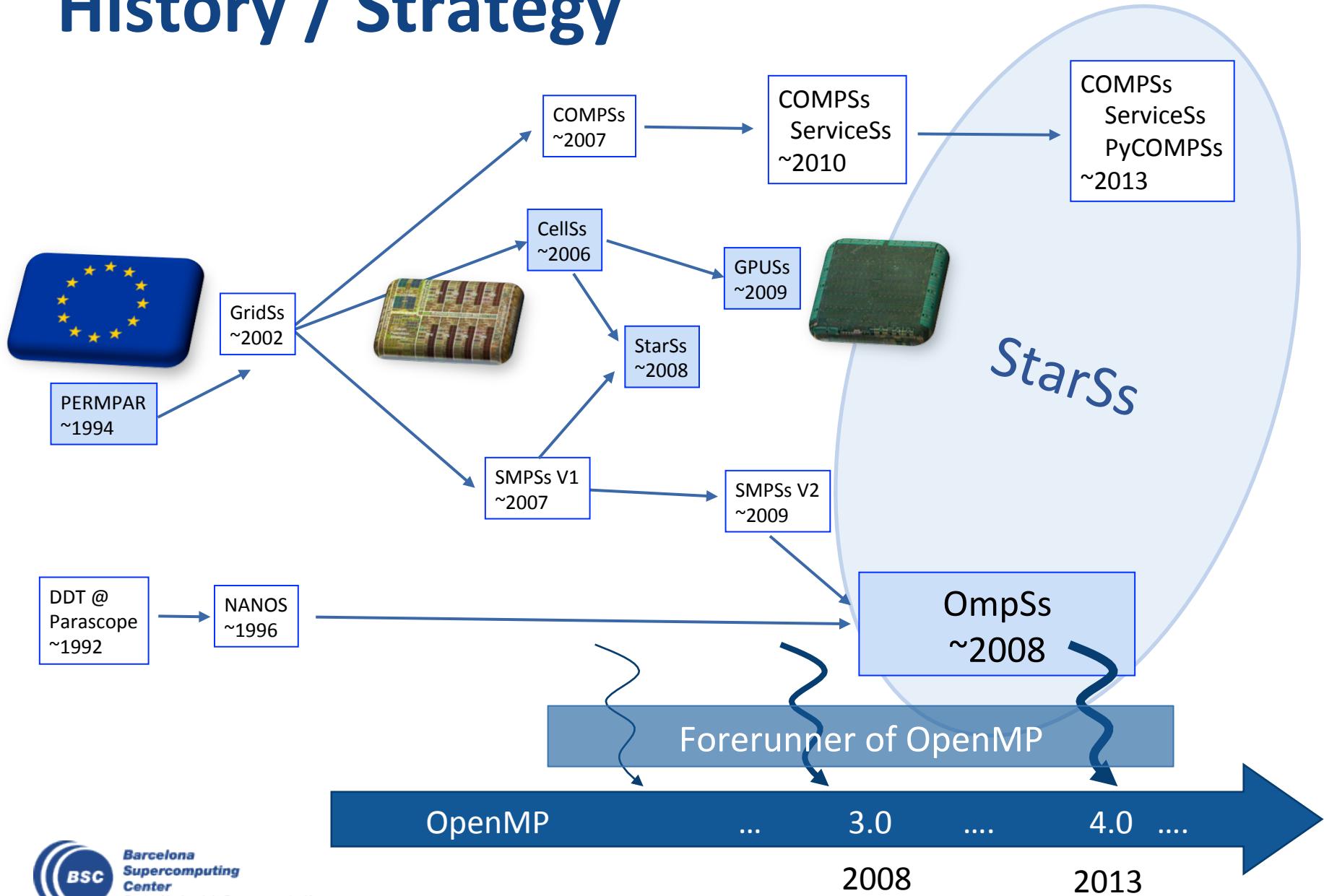


Cristaleria Espanola
UNICAN
CEPBA-UPC



Inisel Espacio
Infocarto
UPC-TSC
CEPBA-UPC

History / Strategy

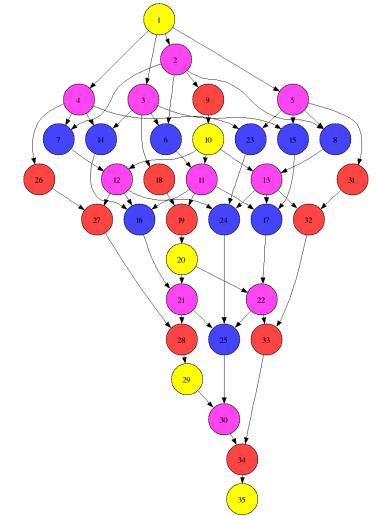


Computer Science: towards Exascale systems

OmpSS

Programming models and runtime

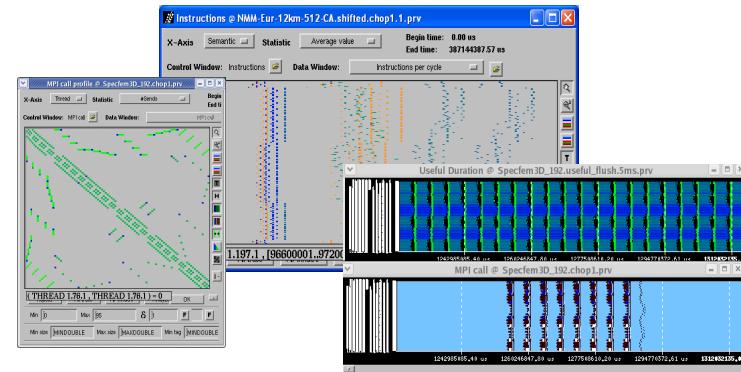
- Simple data directionality annotations for tasks
- Asynchronous data-flow, intelligence to the runtime
- Dynamic Load Balancing (**DLB**)
- Influencing/leveraging standards (OpenMP)
- **MPI + X**, interoperability with accelerator models (CUDA, OpenCL, OpenMP4.0)



BSCTools

Performance analysis tools

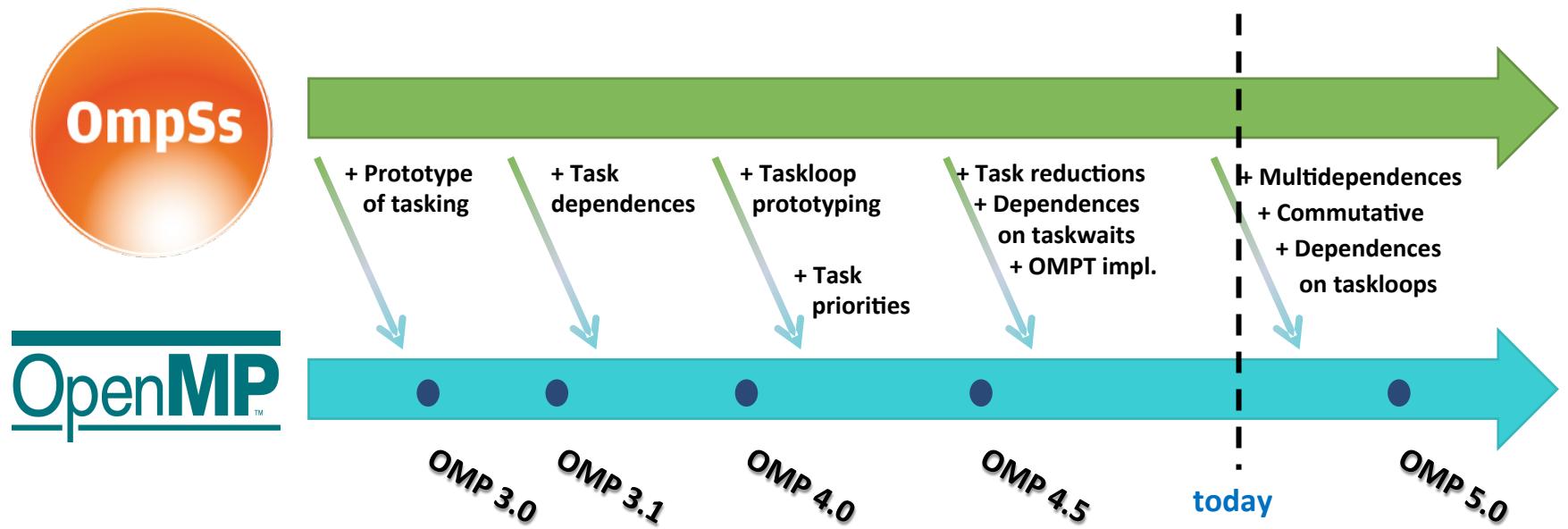
- Extrae, Paraver and Dimemas
- Performance analytics: intelligence, insight
- Methodologies for the performance analyst



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OmpSs

A forerunner for OpenMP



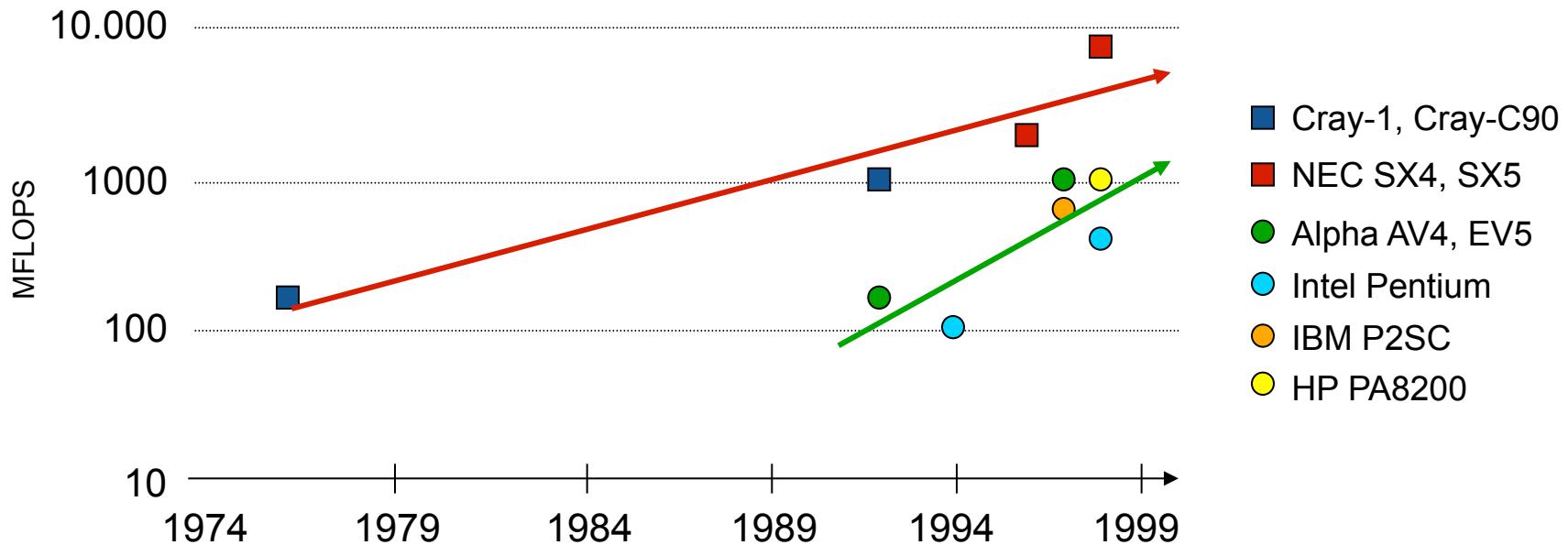
1st European researcher to receive Ken Kennedy Award



2017 Ken Kennedy Award Recipient

"For his contributions to programming models and performance analysis tools for High Performance Computing."

“Killer microprocessors”



- Microprocessors killed the Vector supercomputers
 - They were not faster ...
 - ... but they were significantly **cheaper** and **greener**
- 10 microprocessors approx. 1 Vector CPU
 - SIMD vs. MIMD programming paradigms

M. Valero. “Vector Architectures: Past, Present and Future”. Keynote talk. ICS-11. IEEE-ACM. Melbourne, 1998

Then, commodity took over special purpose



« ASCI Red, Sandia

- 1997, 1 Tflops (Linpack),
- 9298 processors at 200 MHz,
- 1.2 Terabytes
- Intel Pentium Pro
 - Upgraded to Pentium II Xeon, 1999, 3.1 Tflops

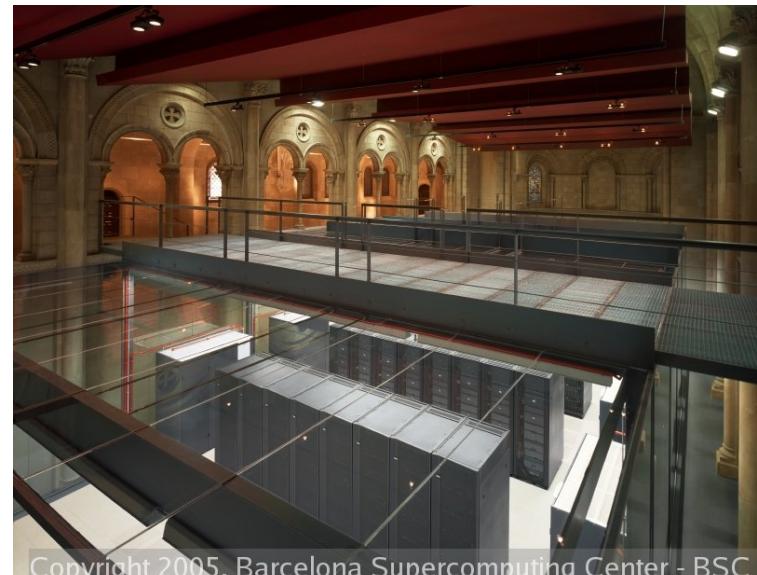
« ASCI White, Lawrence Livermore Lab.

- 2001, 7.3 TFLOPS,
- 8192 proc. RS6000 at 375 MHz,
- 6 Terabytes,
- IBM Power 3
- (3+3) MWats

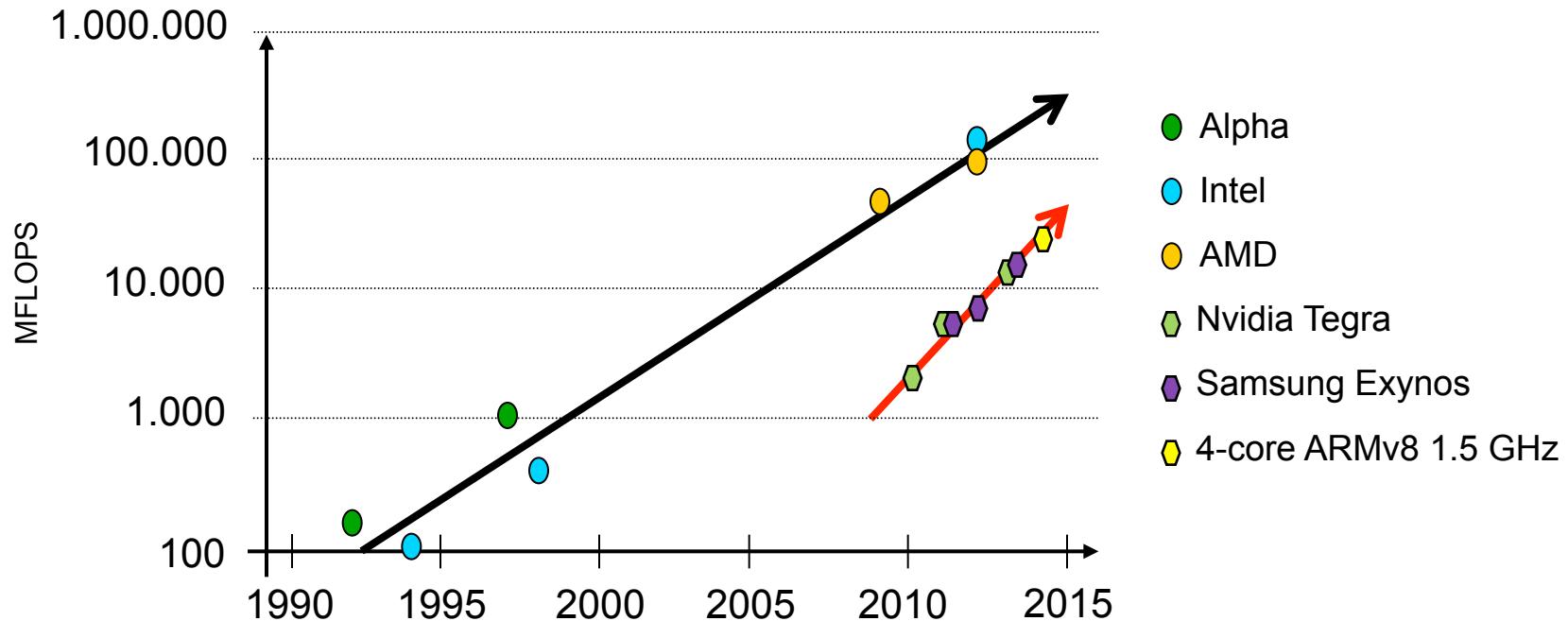
Message-Passing Programming Models

Finally, commodity hardware + commodity software

- MareNostrum
 - Nov 2004, #4 Top500
 - 20 Tflops, Linpack
 - IBM PowerPC 970 FX
 - Blade enclosure
 - Myrinet + 1 GbE network
 - SuSe Linux



The Killer Mobile processors™



- « Microprocessors killed the Vector supercomputers
 - « They were not faster ...
 - « ... but they were significantly cheaper and greener

- « History may be about to repeat itself ...
 - « Mobile processor are not faster ...
 - « ... but they are significantly cheaper and greener

10+ year Horizon Future

- CMOS technology will continue to dominate the market for the next 25 years
 - However, we must be ready for CMOS alternatives
 - Quantum computing, Molecular computing, ...
- Europe is well positioned in embedded processors, applications and technology
 - Tomorrow's embedded processor are today's high-performance processors
- Europe need to remain the leader in the future embedded domain

HiPEAC Objectives

- to help companies identify and select the best architecture solutions for scaling up high-performance embedded processors in the coming years
- to unify and focus academic research efforts through a processor architecture and compiler research roadmap
- to address the increasingly slow progression of sustained processor performance by jointly developing processor architecture and compiler optimizations
- to explore novel approaches for achieving regular and smooth scaling up of processor performance with technology, and to explore the impact of a wide range of post-Moore's law technologies on processor architecture and programming paradigms.

Partners

Leading partner per country

Chalmers University, Sweden

CNRS, France

Delft University, The Netherlands

Edinburgh University, UK

Ghent University, Belgium

INRIA, France

University of Augsburg, Germany

University of Patras, Greece

University of Pisa, Italy

UPC Barcelona

Industrial

STMicro, Switzerland

Infineon, Germany

Ericsson, Sweden

Virtutech, Sweden

IBM Haifa, Israel

Kayser Italia, Italy

Philips Research, The Netherlands



Steering Committee

Josep Llosa
Spain



Olivier Temam
France



Per Stenström
Sweden

Mike O'Boyle
UK



Theo Ungerer
Germany

Coordinator



Mateo Valero
UPC



Stamatis Vassiliadis
The Netherlands



Koen De Bosschere
Belgium



Antonio Prete
Italy



Stefanos Kaxiras
Greece



Network of c.2,000 European R+D experts in advanced computing: **high-performance** and **embedded** architecture and compilation

720 members, 449 affiliated members and 871 affiliated PhD students from **430 institutions** in **46 countries**.



hipec.net



HiPEAC has received funding from the European Union's Horizon2020 research and innovation programme under grant agreement number 779656.



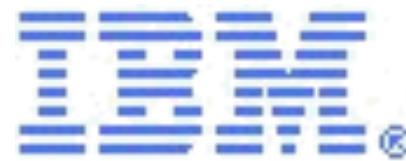
Centre de
Computació i
Comunicacions de
Catalunya



CEPBA

CECESCA

CEPBA-IBM Research Institute

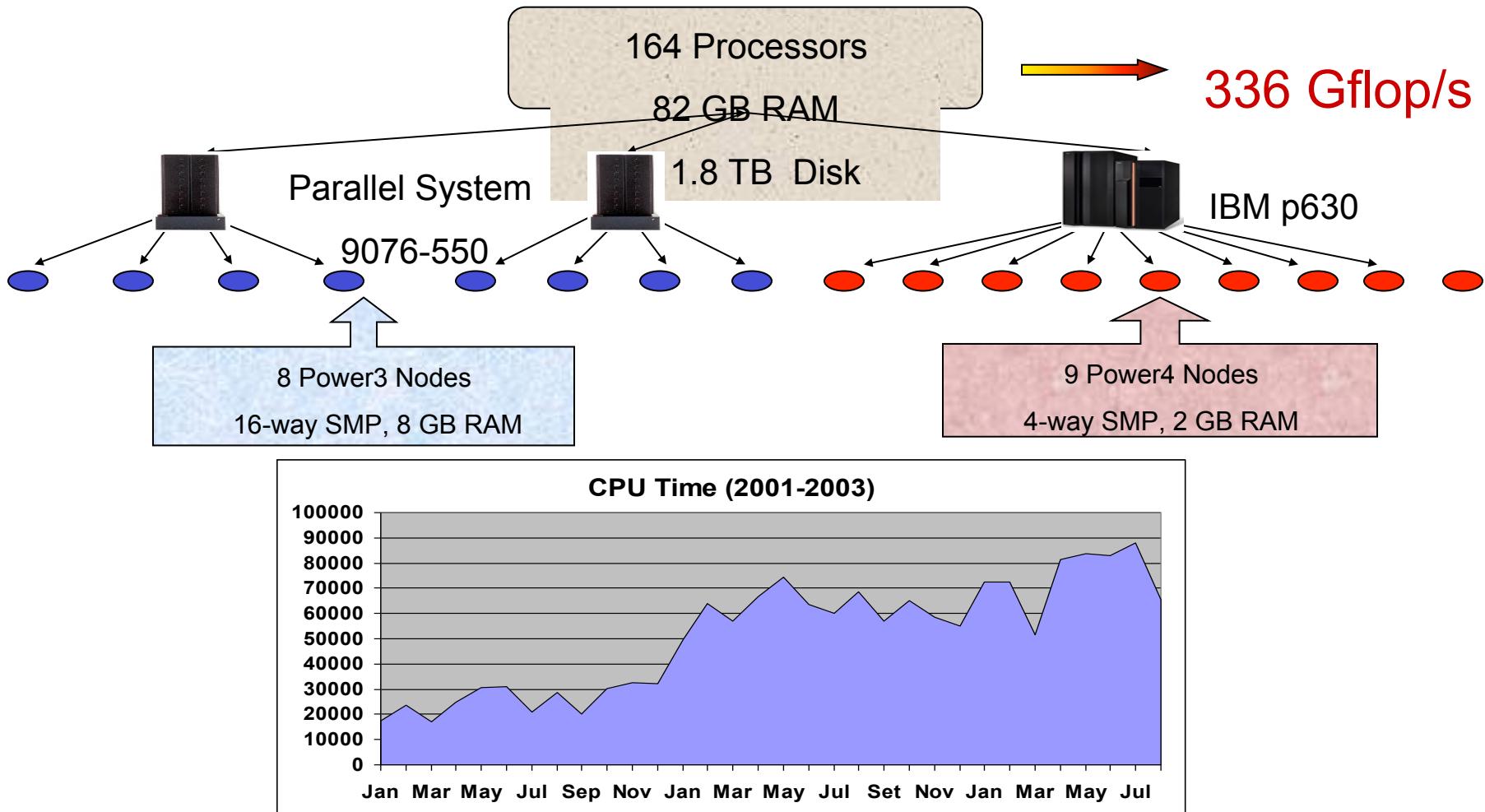


CIRI's mission

CEPBA-IBM Research Institute (CIRI) is a research and development partnership between UPC and IBM.

- Established in October 2000 with an initial commitment of four years.
- Its mission is to contribute to the community through R&D in Information Technology.
- Its objectives are:
 - Research & Development
 - External R&D Support
 - Technology Transfer
 - Education
- CEPBA (European Center for Parallelism Barcelona) is a deep computing research center, at the Technical University of Catalonia (UPC), which was created in 1991.

Resources / CPU user time evolution



MareIncognito: Project structure



4 relevant apps:

- Materials: SIESTA
- Geophysics imaging: RTM
- Comp. Mechanics: ALYA
- Plasma: EUTERPE

General kernels

Applications

Programming models

Performance analysis tools

Models and prototype

Load balancing

Interconnect

Processor and node

- Contention, Collectives
- Overlap computation/communication
- Slimmed Networks
- Direct versus indirect networks

StarSs: CellSs, SMPSS
OpenMP@Cell
OpenMP++
MPI + OpenMP/StarSs

Coordinated scheduling:
Run time,
Process,
Job
Power efficiency

- Contribution to new Cell design
- Support for programming model
- Support for load balancing
- Support for performance tools
- Issues for future processors

« El CEPBA-IBM Research Institute fue creado en Octubre del 2000.



CESBA... luego BSC

- Centro Español de Supercomputación de Barcelona

Centro Español de Supercomputación

La creación de un Centro Nacional de Supercomputación, con capacidad de I+D en Tecnologías de la Información y de soporte a la I+D de la comunidad científico-técnica española, contribuirá de forma significativa al desarrollo de la ciencia en España, obtendrá economías de escala en la capacidad de computación, sinergias científico-técnicas y racionalización de costes.

El Centro contribuirá a impulsar un entorno tecnológico adecuado en el que las administraciones públicas, los centros de investigación y las empresas puedan beneficiarse de las ventajas de la supercomputación, y tratar de resolver los problemas científicos más acuciantes de nuestros días.

Supercomputación

La Supercomputación es una herramienta de ayuda al desarrollo de la ciencia y la ingeniería, concretamente en las áreas de simulación y modelización.

Casos concretos:

- **ciencias de la vida, ayuda a mejorar la salud humana gracias a la simulación virtual de los procesos que suceden en el cuerpo humano**
- **ciencias del medio ambiente, herramienta para combatir tres de las grandes amenazas del s.XXI: la falta de agua, la extensión de las enfermedades infecciosas y el cambio climático**
- **ciencias químico-físicas, nuevos materiales y nuevos diseños en aeronáutica, o automoción , (aplicación al barco español, si participamos en la Copa del América?)**

La Supercomputación es una herramienta poderosa para aumentar la competitividad científica, tecnológica e industrial de las comunidades que invierten en ella.

Beneficios para la Comunidad Científica

El Centro Nacional de Supercomputación permitirá a los científicos españoles acceder a recursos de los que hoy sólo disponen en centros internacionales, ampliando el espectro de proyectos abordables y acelerándose los ciclos de investigación actuales.

El superordenador estará disponible también para la comunidad científica internacional, ayudando a aumentar la ventaja competitiva de nuestro país en el nuevo mundo tecnológico.

El uso del supercomputador del Centro se establecerá en base a un protocolo abierto de acceso disponible para todos los investigadores españoles. Un Consejo Científico independiente priorizará los proyectos de investigación a aceptar según su valor científico y su necesidad real de supercomputación.

Previsión para el siguiente TOP500 (Nov. 2004)

El Superordenador tendrá una capacidad de proceso de 40 TFlop/s pico (>20 sustained) y constará aproximadamente de 4.500 procesadores, 128 TeraBytes de disco y 9.0 TeraBytes de memoria principal.

System	Make	Procs	Description	Type	Linpack TFlops	MW	Country
#1 Blue Gene L (1/2)	IBM	64k	750 MHz POWER	special purpose	100	.8	USA
#2 Earth Simulator	NEC	5120	500 MHz NEC	special purpose	35.9	5.1	Japan
#3 or #4 Red Storm	CRAY	10k	2 GHz Opteron	special purpose	20-35 (40 Peak)	< 2	USA
#4 or #3 Mare Nostrum	IBM	4500	2.2 GHz POWER	general purpose	20+	< 0.3	Spain
#5 ASCI Q	HP	8192	1.25 GHz Alpha	special purpose	13.9	2	USA
#6 Big Mac	Apple	2200	2 GHz POWER	homebuilt	9.8	< 1.5	USA

MareNostrum

« MareNostrum fue instalado en 2004 a partir de la firma entre el Gobierno de España e IBM de 10 de marzo de 2004.



MareNostrum

« MareNostrum fue instalado en 2004 a partir de la firma entre el Gobierno de España e IBM de 10 de marzo de 2004.



MareNostrum

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MareNostrum

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El Barcelona Supercomputing Center – Centro Nacional de Supercomputación (BSC-CNS) fue constituido el 1 de Abril de 2005



« El Barcelona Supercomputing Center – Centro Nacional de Supercomputación (BSC-CNS) fue constituido el 1 de Abril de 2005



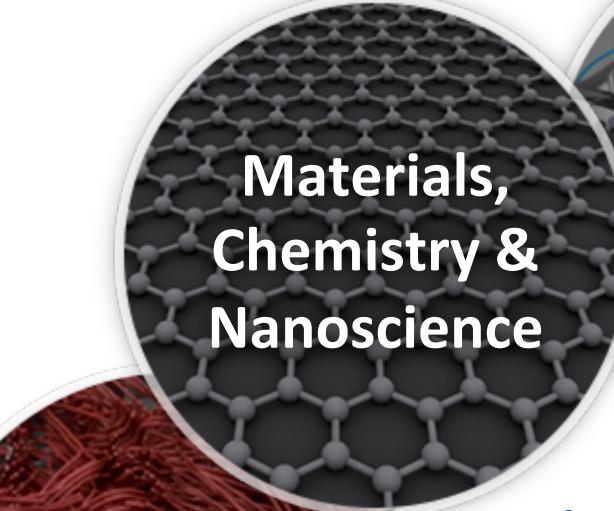
The evolution of the research paradigm



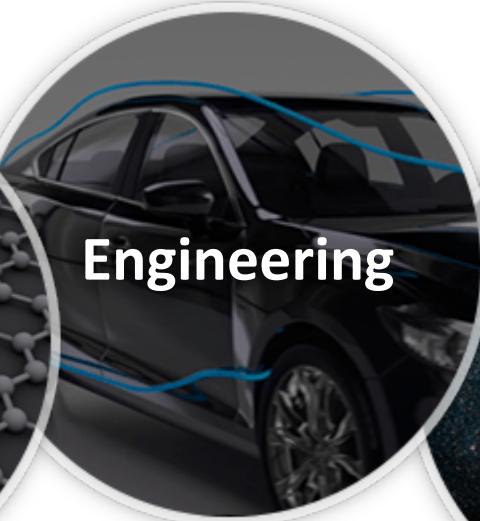
Numerical
simulation and
Big Data analysis

- Reduce expense
- Avoid suffering
- Help to build knowledge where experiments are impossible or not affordable

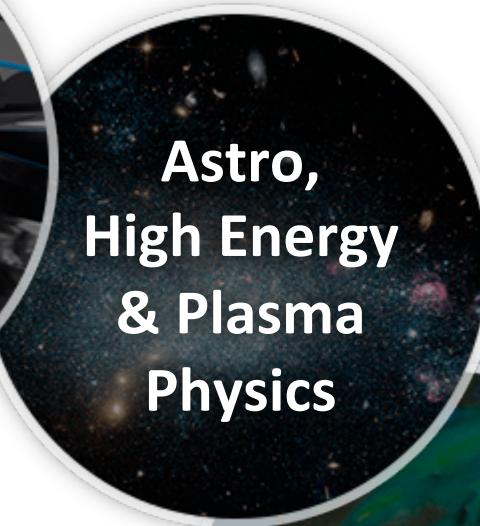
HPC: An enabler for all scientific fields



Materials,
Chemistry &
Nanoscience



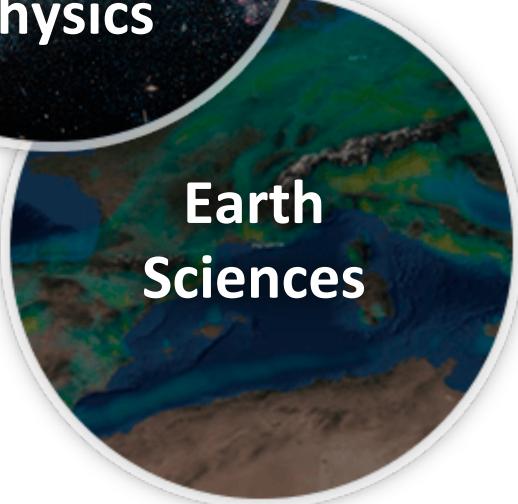
Engineering



Astro,
High Energy
& Plasma
Physics



Life Sciences
& Medicine



Earth
Sciences

Advances leading to:

- Improved Healthcare
- Better Climate Forecasting
- Superior Materials
- More Competitive Industry



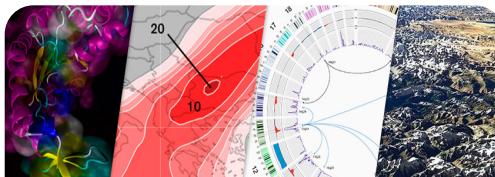
Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación

Barcelona Supercomputing Center

Centro Nacional de Supercomputación



Supercomputing services
to Spanish and
EU researchers



R&D in Computer,
Life, Earth and
Engineering Sciences



PhD programme,
technology transfer,
public engagement



Spanish Government

60%



Catalonian Government

30%



Univ. Politècnica de Catalunya (UPC)

10%



MareNostrum4

Total peak performance: **13,7 Pflops**

General Purpose Cluster: 11.15 Pflops (1.07.2017)

CTE1-P9+Volta: 1.57 Pflops (1.03.2018)

CTE2-Arm V8: 0.5 Pflops (?????)

CTE3-KNH?: 0.5 Pflops (?????)



MareNostrum 1

2004 – 42,3 Tflops

1st Europe / 4th World
New technologies

MareNostrum 2

2006 – 94,2 Tflops

1st Europe / 5th World
New technologies

MareNostrum 3

2012 – 1,1 Pflops

12th Europe / 36th World

MareNostrum 4

2017 – 11,1 Pflops

2nd Europe / 13th World
New technologies

Top10, Nov 2018

Rank	Name	Site	Manufacturer	Country	Cores	Accelerators	Rmax [TFlop/s]	Rpeak [TFlop/s]	GFlops /Watts
1	Summit	DOE/SC/Oak Ridge National Laboratory	IBM	US	2,397,824	2,196,480	143,500	200,795	14.67
2	Sierra	DOE/NNSA/Lawrence Livermore National Lab.	IBM/NVIDIA	US	1,572,480	1,382,400	94,640	125,712	12.72
3	Sunway TaihuLight	National Supercomputing Center in Wuxi	NRCPC	China	10,649,600		93,015	125,436	6.05
4	Tianhe-2 ^a	National Super Computer Center in Guangzhou	NUDT	China	4,981,760	4,554,752	61,445	100,679	3.33
5	Piz Daint	Swiss National Supercomputing Centre	Cray Inc.	Switz	387,872	319,424	21,230	27.154.3	8.90
6	Trinity	DOE/NNSA/LANL/SNL	Cray Inc.	US	979,072		20,158,7	41,461	2.66
7	AI Bridging Cloud Inf.	National Inst. of Adv Industrial Science & Tech.	Fujitsu	Japan	391,680	348,160	19,880	32,577	12.05
8	SuperMUC-NG	Leibniz Rechenzentrum	Lenovo	Germany	305,856		19,477	28,872,86	
9	Titan	DOE/SC/Oak Ridge National Laboratory	Cray Inc.	US	560,640	261,632	17,590	27,113	2.14
10	Sequoia	DOE/NNSA/Lawrence Livermore National Lab.	IBM	US	1,572,864		17,173	20,133	2.18
25	MareNostrum	Barcelona Supercomputing Center	Lenovo	Spain	153,216		6,471	10,296	3.97

MareNostrum 4



From MN3 to MN4

MareNostrum 4, chosen as the most beautiful data centre in the world

11 December 2017

The award, organised by DataCenter Dynamics, has been granted by popular vote.



MareNostrum 4 supercomputer has been the winner of the Most Beautiful Data Center in the world Prize, hosted by the [Datacenter Dynamics \(DCD\) Company](#).

There are 15 prizes in different categories, besides the prize for the most beautiful data centre, which is elected by popular vote. MareNostrum 4 competed with such impressive facilities as the Switch Pyramid in Michigan, the Bahnhof Pionen in Stockholm or the Norwegian Green Mountain. BSC supercomputer has prevailed for its particular location, inside the chapel of Torre Girona, located in the North Campus of the Universitat Politècnica de Catalunya (UPC).

The awards ceremony took place on December 7th in London and both Mateo Valero, BSC Director, and Sergi Girona, Operations department Director, received the prize.

About MareNostrum 4

Mission of BSC Scientific Departments

Computer Sciences

To influence the way machines are built, programmed and used: programming models, performance tools, Artificial Intelligence, computer architecture, energy efficiency

Life Sciences

To understand living organisms by means of theoretical and computational methods
(molecular modeling, genomics, proteomics)

Earth Sciences

To develop and implement global and regional state-of-the-art models for short-term air quality forecast and long-term climate applications

CASE

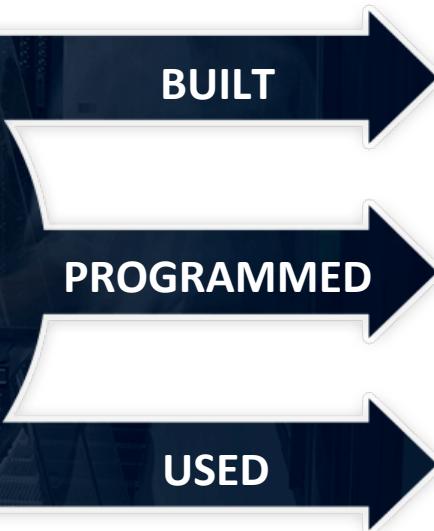
To develop scientific and engineering software to efficiently exploit super-computing capabilities
(biomedical, geophysics, atmospheric, energy, social and economic simulations)



Computer Sciences



Influence
the way
machines
are



Holistic Computer
Architecture Research

20 years innovating in
Programming Models

Performance Analytics
Tools: From Data to Insight



- Race towards Exascale
- HPC and Big Data convergence
- Artificial Intelligence era
- Multiple architectures for different applications domains

Life Sciences

Understanding living organisms by theoretical and computational methods

Machine Learning

Computational
genomics



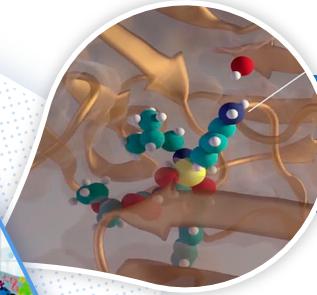
Personalized
Medicine

Protein and
drug modeling

Text
Mining



Bio-
Infrastructure



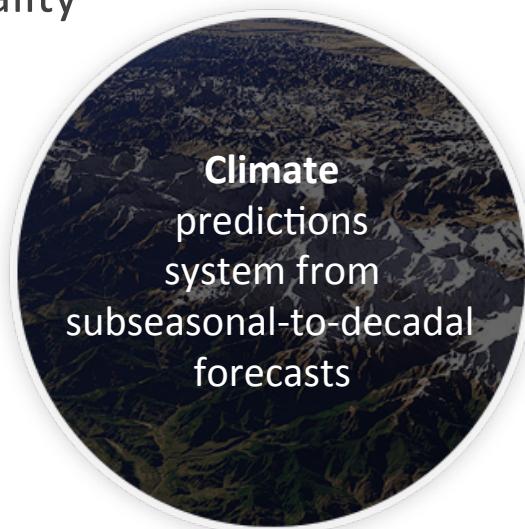
Evaluation of
social impact



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación

Earth Sciences

Environmental modelling and forecasting, with a particular focus on weather, climate and air quality



Service Users Sectors



Infrastructures



Solar Energy



Urban development



Transport



Wind Energy



Agriculture



Insurance

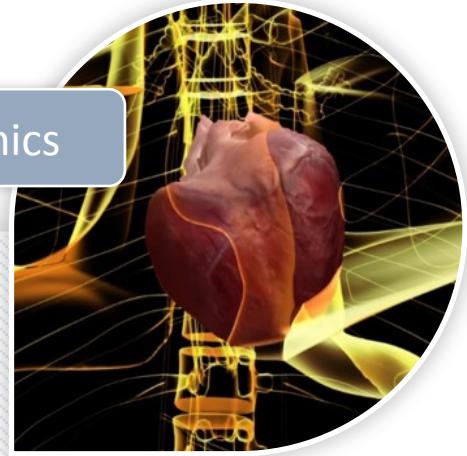


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Supercomputing
Center
Centro Nacional de Supercomputación

Computational Applications for Science and Engineering



Energy

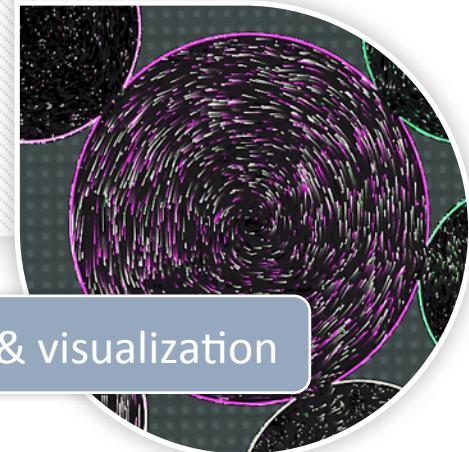


Biomechanics

INDUSTRY
ORIENTED
DEPARTMENT



Smart & resilient cities

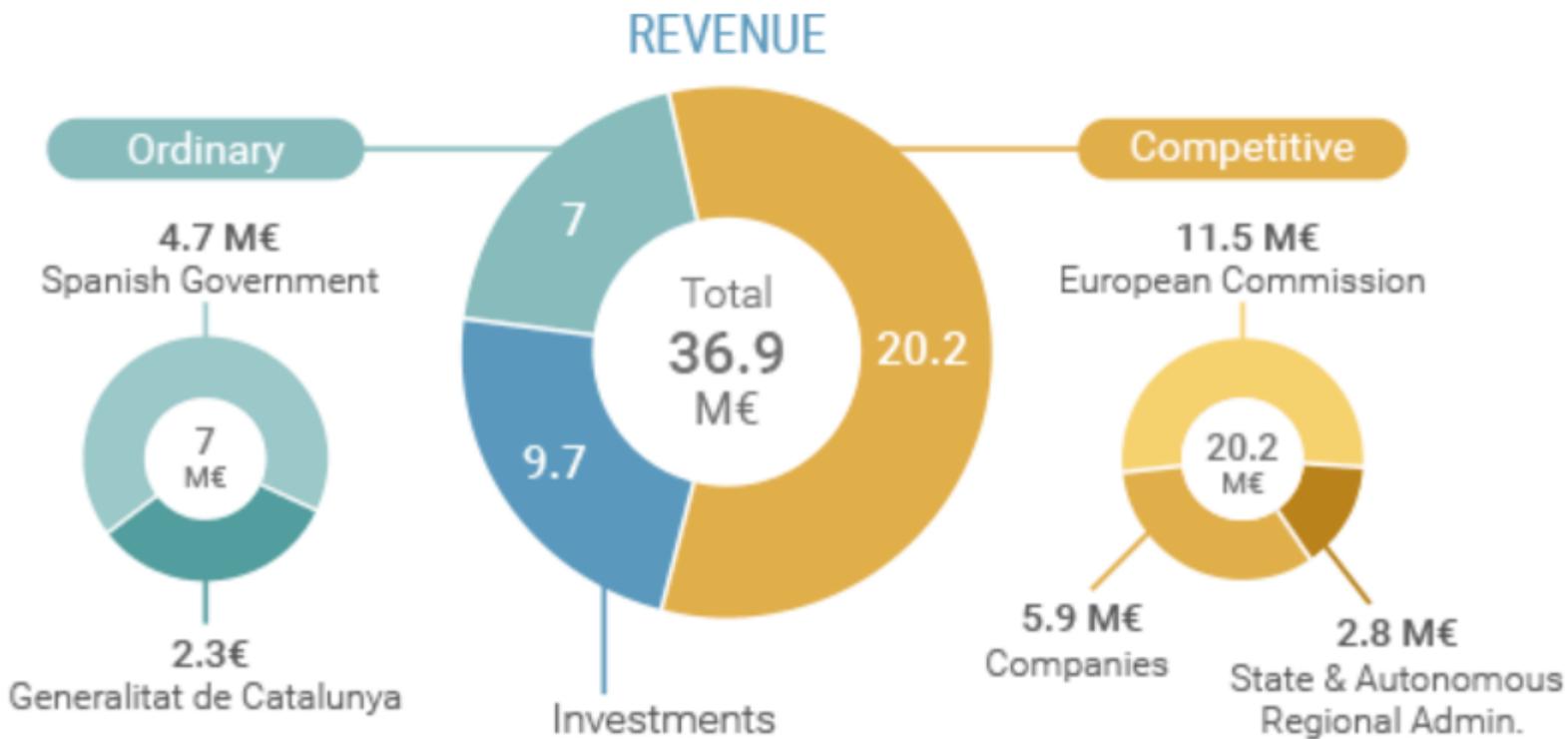


Data analytics & visualization



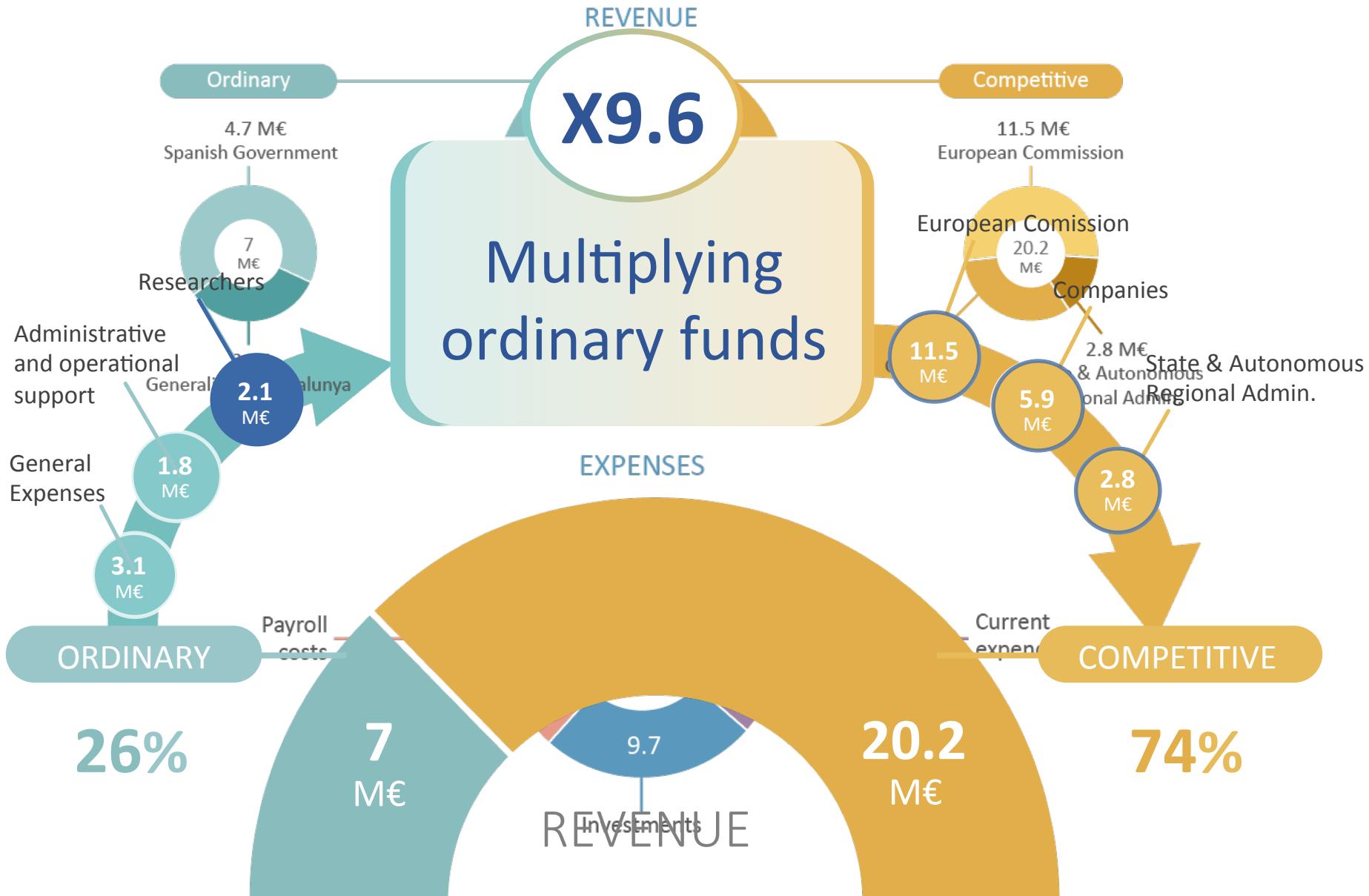
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Centro Nacional de Supercomputación

BSC Funding 2017

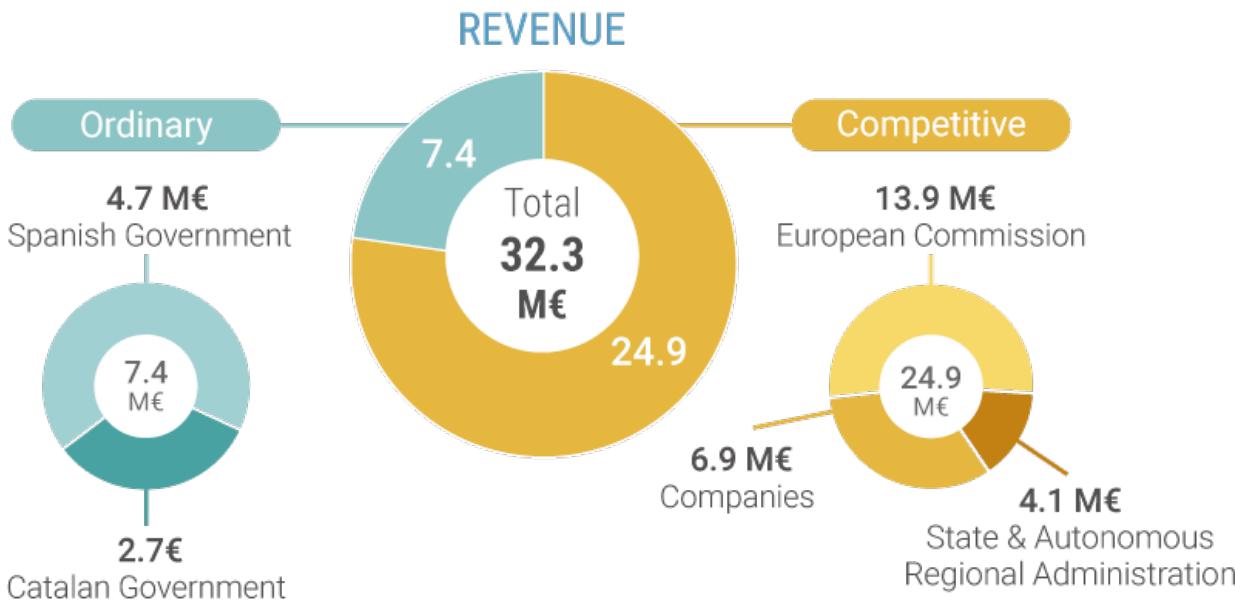


BSC Resources

2017 executed budget



BSC Funding 2018

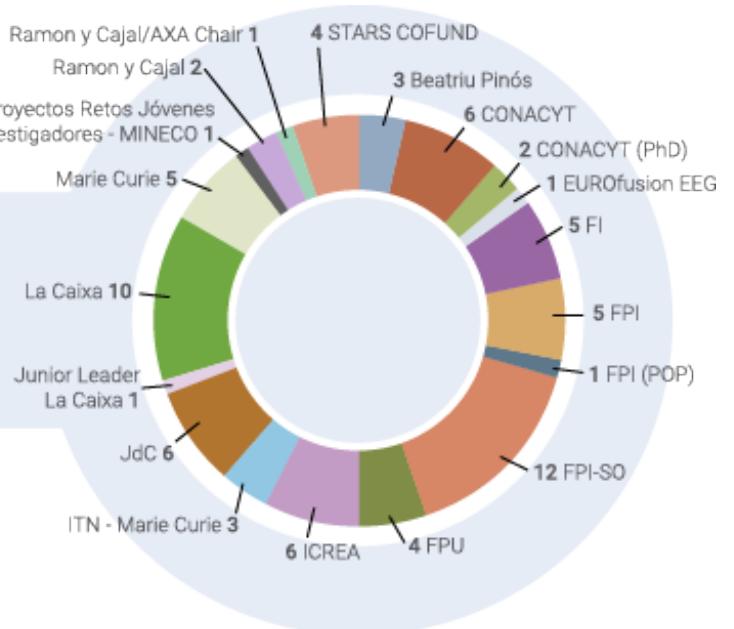
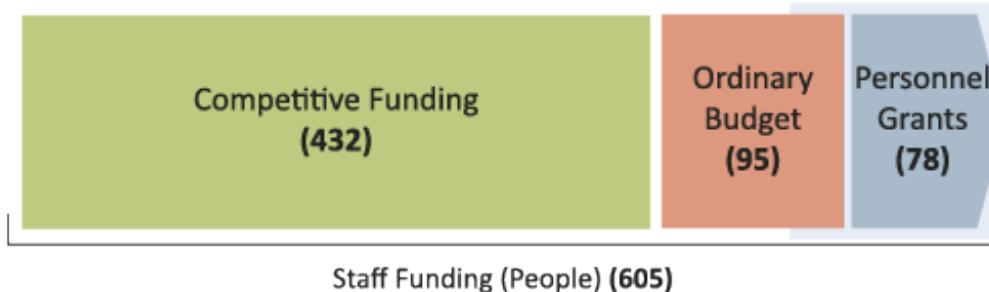
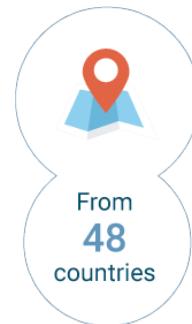
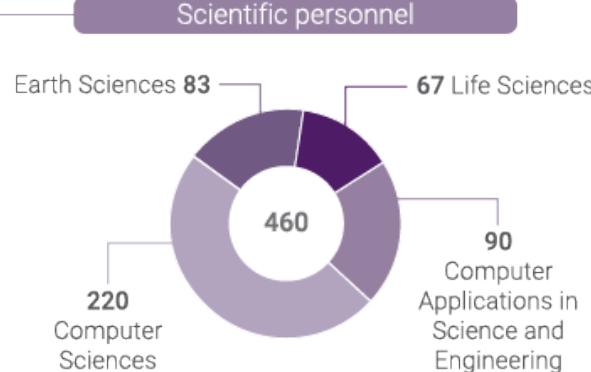
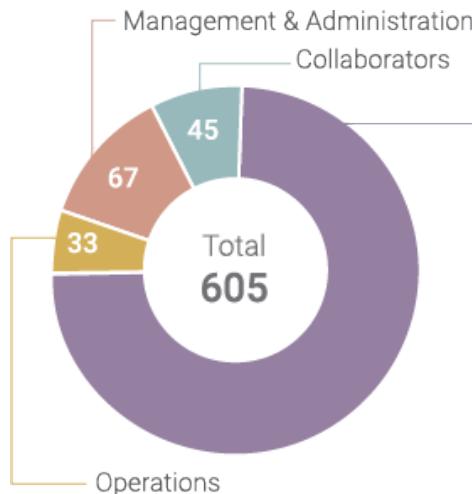


INVESTMENTS

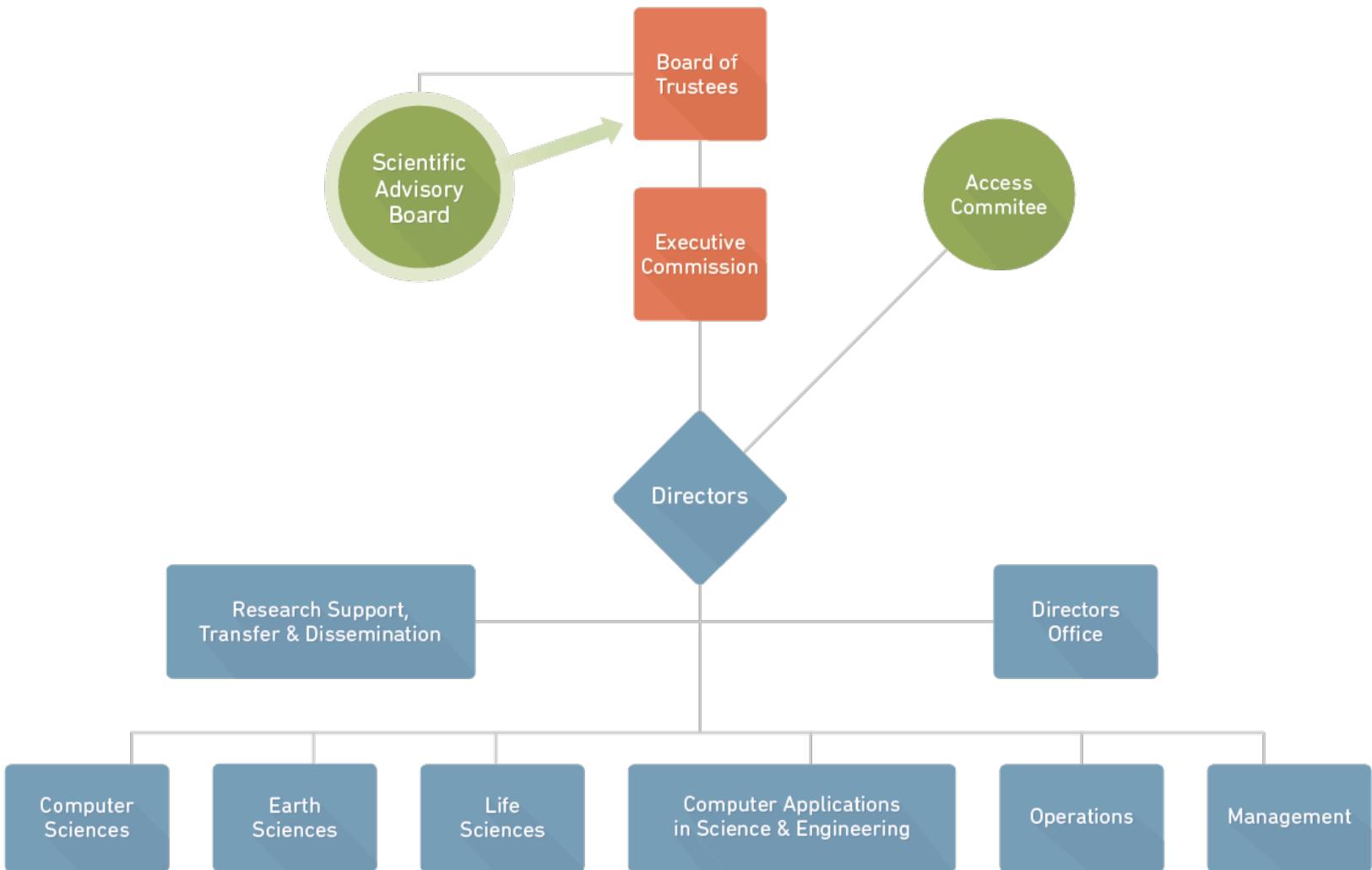
9 M€
MareNostrum 4

0.9 M€
Assets in Progress

People



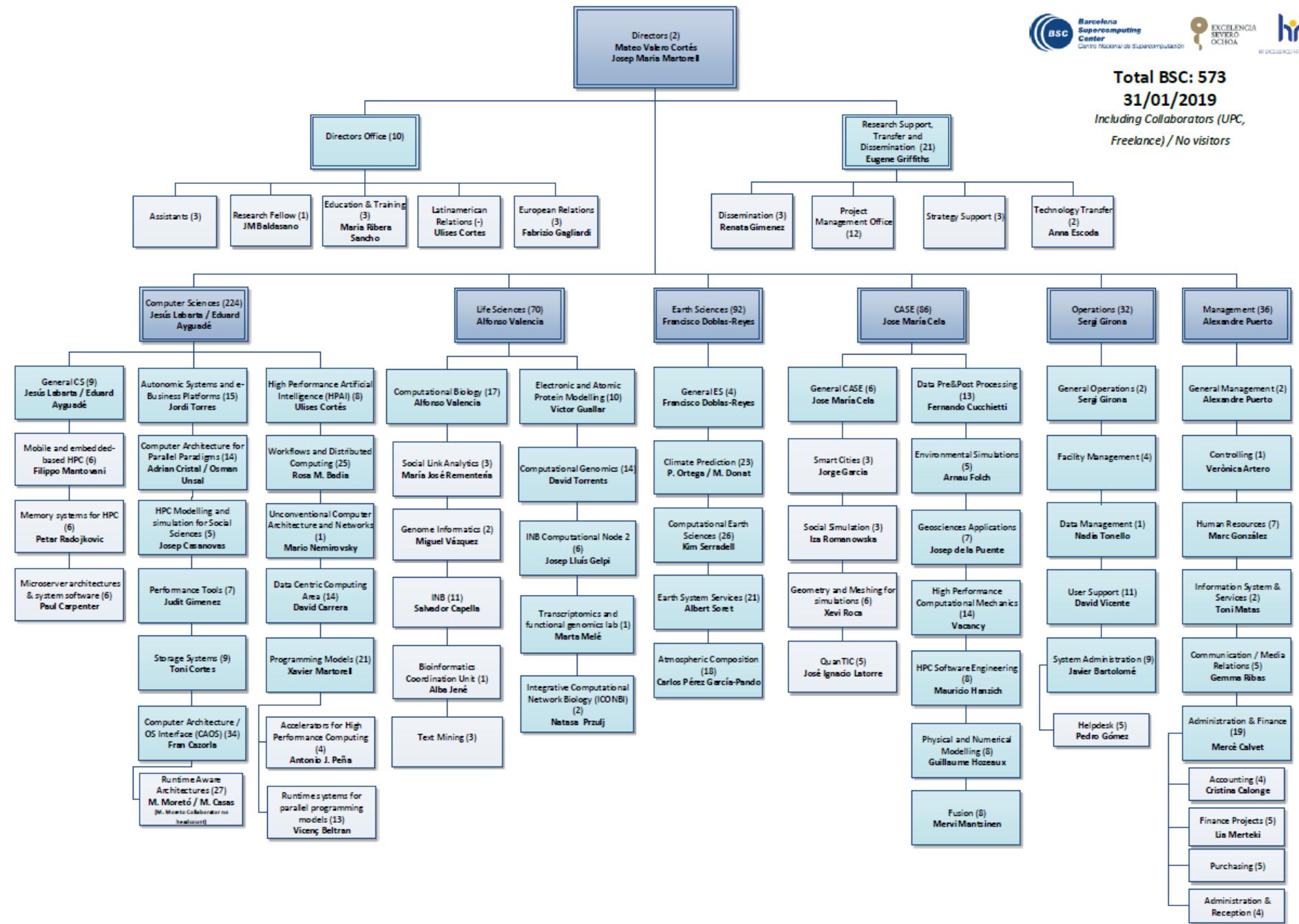
Organization updated



Total BSC: 573

31/01/2019

*Including Collaborators (UPC,
Freelance) / No visitors*



ERC and ICREA at BSC

**Mateo Valero**

research in microarchitecture, runtime systems, compilers and programming languages

**Xevi Roca**

research in best curved adapted meshes for space-time flow simulations

**Fran Cazorla**

research in Sustainable Performance for High Performance Embedded Computing Systems

**David Carrera**

research in holistic integration of emerging supercomputing technologies

**Victor Guallar**

research in electronic and atomic protein modelling

**Nataša Pržulj**

research in biological network topology as a complement of genome biological information

**Toni Gabaldón**

research in reticulate evolution: patterns and impacts of non-Vertical inheritance in eukaryotic genomes

**Alfonso Valencia**

Research in text mining technology and protein interaction networks applied to Precision Medicine

**David Torrents**

research in computational genomics

**Mervi Mantinen**

research in numerical modelling of experiments in magnetically confined fusion devices

**Francisco J Doblas-Reyes**

research in earth software, Climate Change and seasonal-to-decadal predictions

**Vassil Alexandrov**

research in extreme computing

**Mario Nemirovsky**

research in unconventional computer architecture and networks

People evolution

BSC Staff 2005 - 2018



RES: HPC Services for Spain

RES now made up of **thirteen** supercomputers



- **Finis Terrae II**, Centro de Supercomputación de Galicia (CESGA);
- **Pirineus**, Consorcio de Servicios Universitarios de Cataluña (CSUC);
- **Lusitania**, Fundación Computación y Tecnologías Avanzadas de Extremadura;
- **Caléndula**, Centro de Supercomputación de Castilla y León,y
- **Cibeles**, Universidad Autónoma de Madrid



Access

www.res.es

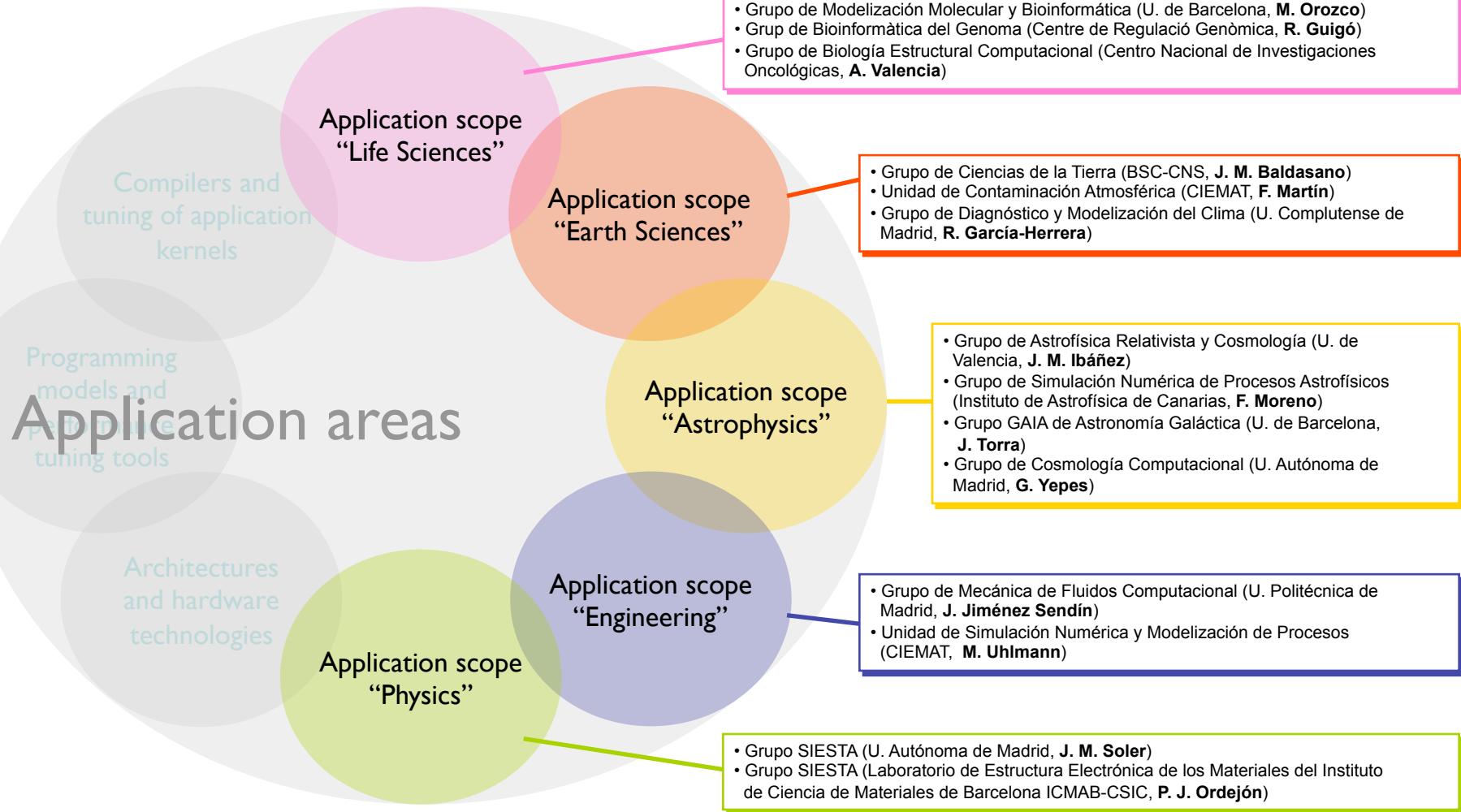


Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación





El BSC-CNS coordina un programa Consolider de supercomputación y e-Ciencia, que une a grupos de investigación expertos en aplicaciones que requieren supercomputación y a grupos expertos en el diseño del hardware y software de base de los supercomputadores.



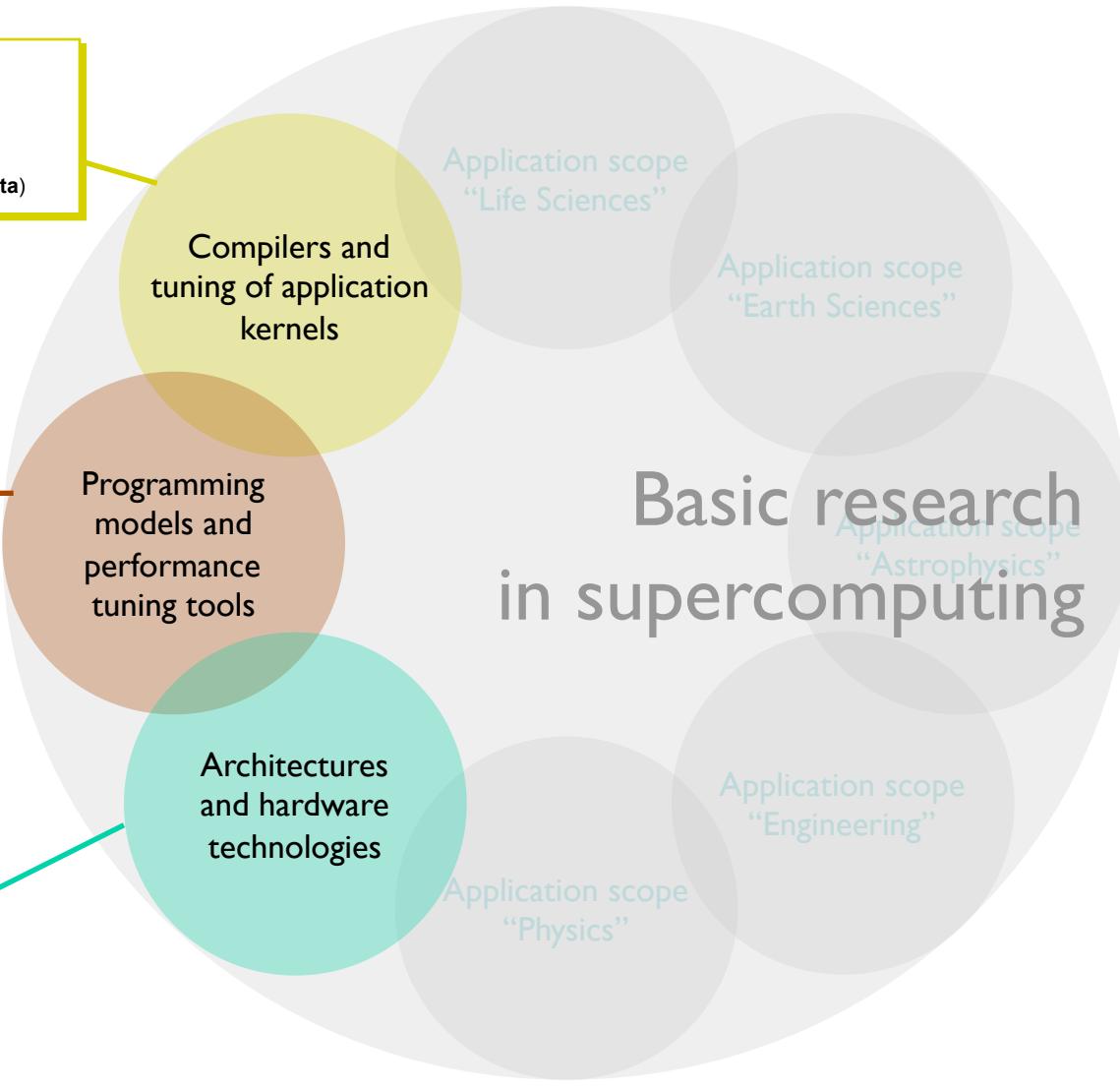
Consolider (2)



- Departamento de Tecnologías de la Información (BSC-CNS, **M. Valero**)
- Grupo Computación de Altas Prestaciones (U. Politècnica de Catalunya, **J. M. Llaberia**)
- Grupo de Arquitectura y Tecnología de Sistemas Informáticos (U. Complutense de Madrid, **F. Tirado**)
- Grupo de Arquitectura de Computadores (U. de Málaga, **E. López Zapata**)

- Departamento de Tecnologías de la Información (BSC-CNS, **M. Valero**)
- Grupo Computación de Altas Prestaciones (U. Politècnica de Catalunya, **J. M. Llaberia**)
- Parallel Processing and Distributed Systems group (U. Autònoma de Barcelona, **A. Ripoll**)

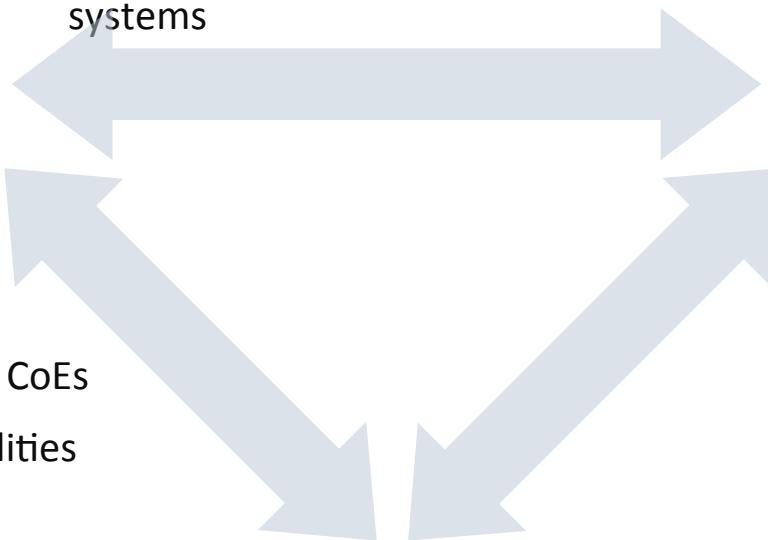
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- Grupo Computación de Altas Prestaciones (U. Politècnica de Catalunya, **J. M. Llaberia**)
- Grupo de Arquitectura de Computadores (U de Zaragoza, **V. Viñals**)
- Grupo de Arquitectura y Tecnología de Sistemas Informáticos (U. Complutense de Madrid, **F. Tirado**)
- Grupo de Arquitectura y Tecnología de Computadores (U. de Cantabria, **J. R. Beivide**)
- Grupo de Arquitectura de Computadores (U. de Málaga, **E. López Zapata**)
- Grupo de Arquitectura de Computadores (U. de Las Palmas de Gran Canaria, Instituto Universitario de Ciencias y Tecnologías Cibernéticas, **E. Fernández**)



EU HPC Ecosystem



- Collaboration of HPC Supercomputing Centres and application CoEs
- Provision of HPC capabilities and expertise



- Specifications of exascale prototypes
- Technological options for future systems
- Identify applications for co-design of exascale systems
- Innovative methods and algorithms for extreme parallelism of traditional & emerging applications

Centers of Excellence in HPC applications



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación



Distributed Supercomputing Infrastructure

25 members, including

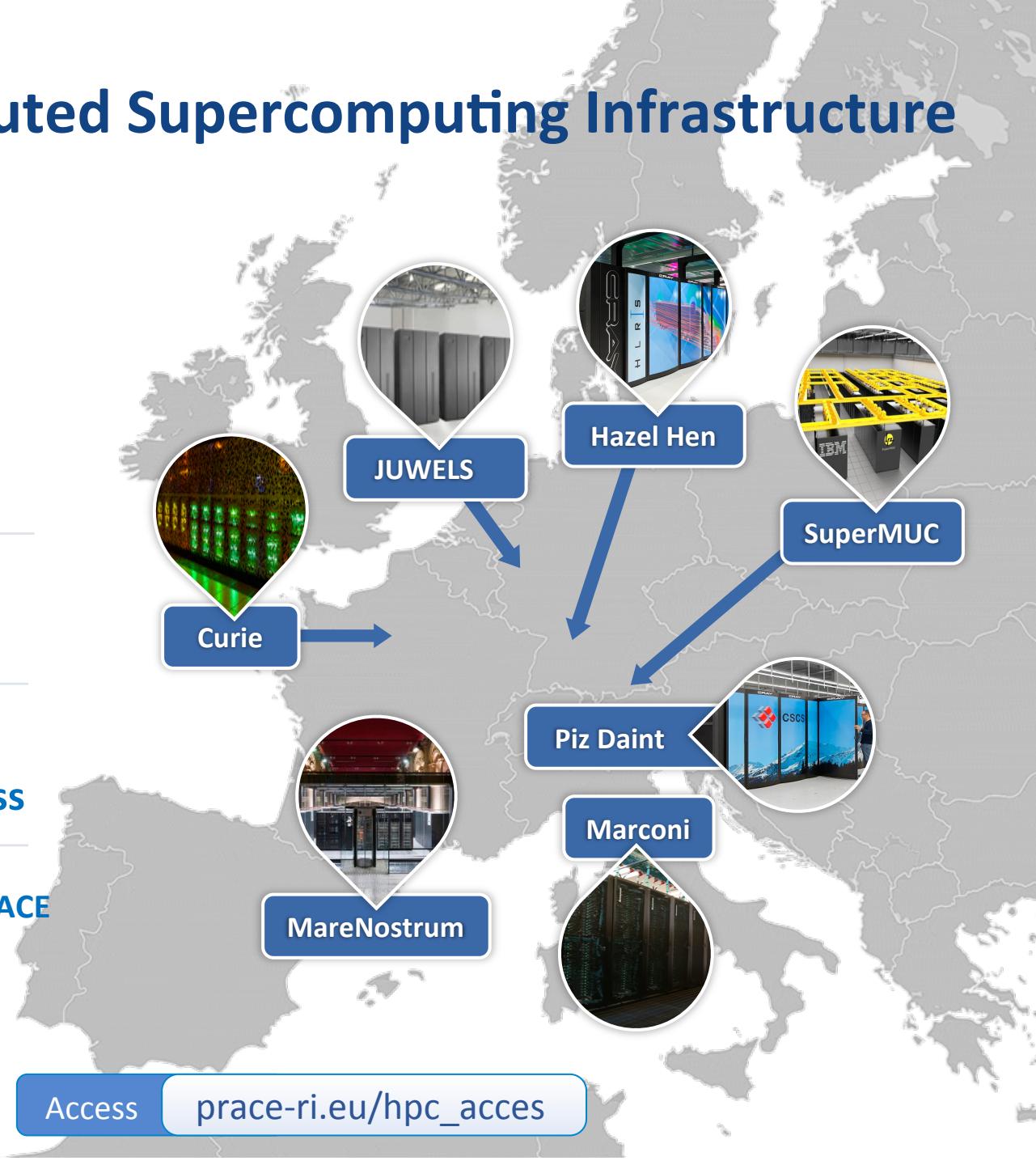
5 Hosting Members

(Switzerland, France, Germany,
Italy and Spain)

**610 scientific projects
enabled**

110 PFlops/s of peak
performance on **7 world-class
systems**

>11.500 people trained by **6 PRACE
Advanced Training Centers** and
others events



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación

Access

prace-ri.eu/hpc_acces

Centers of Excellence in HPC applications

Ten new Centres of Excellence (CoEs) will help strengthen Europe's leadership in HPC applications and cover important areas, providing services such as: developing, optimising and scaling HPC; co-design of hardware, software and codes; consultancy to industry and SMEs; research in HPC Applications.



The BSC – CNS participates in 8 out of 9 and leads 2 of them

Open Project Portfolio December 2018

(includes H2020 projects in negotiation and national projects beginning soon)

234 running projects, total budget of over 97M€*

106 <u>H2020/FP7</u> projects running or in negotiation (11 as coordinator)	62M€
39 running <u>contracts</u>, mostly with companies	12M€
52 projects with <u>Spanish and Catalan</u> public funding	16M€
17 projects funded through <u>other EU programmes</u> (incl. Copernicus)	3M€
17 projects funded through <u>other sources</u>	3M€
3 strategic <u>self-funded</u> projects	1M€

Plus:

65 personnel grants (national funding plus CONACYT programme)	7M€
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Does not include ICREA, UPC, MSC,

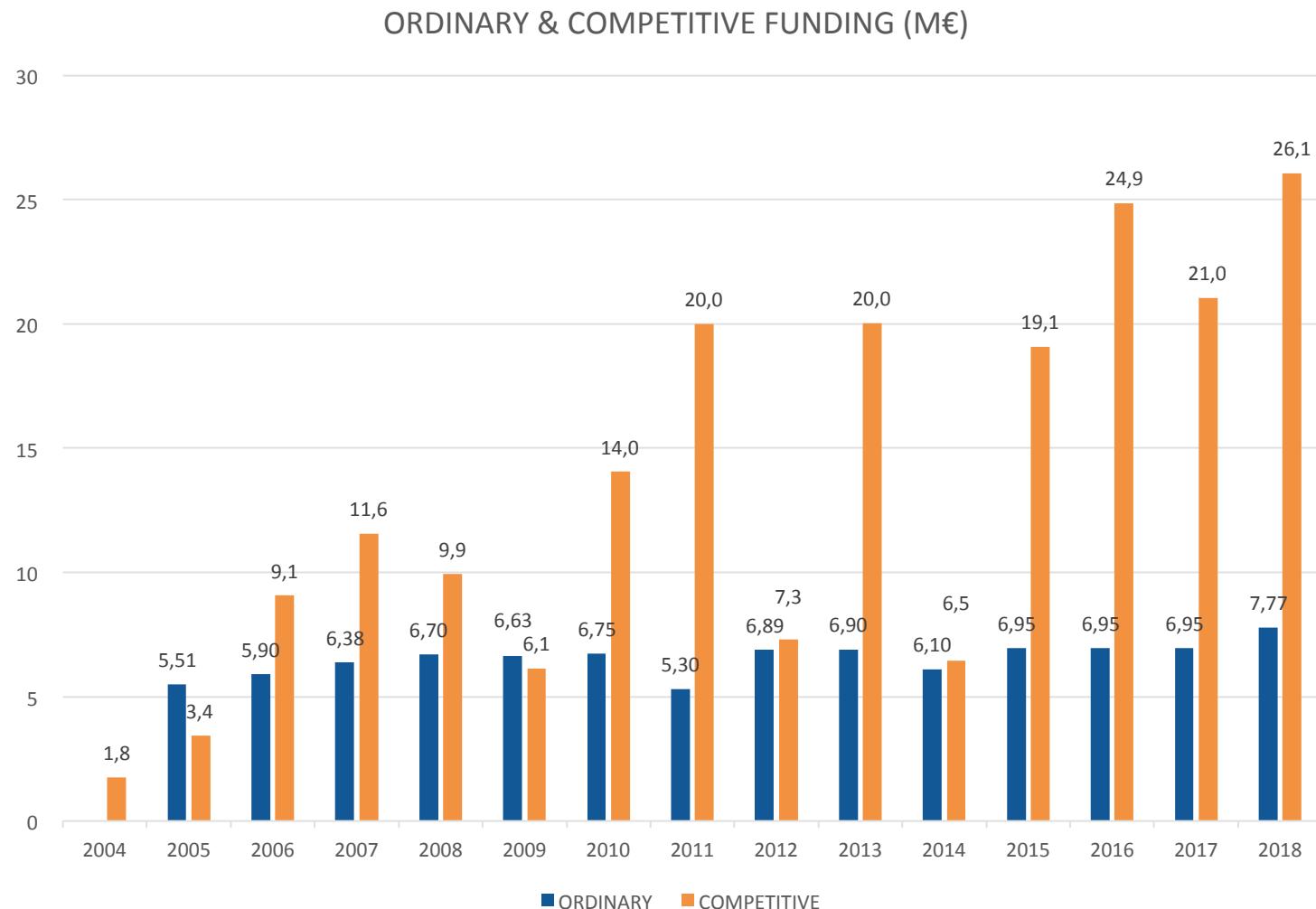
TOP-10 Spanish Organizations in Horizon 2020

Legal name	EU Contribution (€)	Project Participations
CSIC	200,046,801 €	476
Tecnalia	97,873,597 €	219
Barcelona Supercomputing Center	68,326,411 €	123
ATOS	53,654,875 €	134
Universitat Pompeu Fabra	53,189,145 €	96
Universitat Politècnica de Catalunya	52,595,046 €	147
ICFO	50,390,271 €	72
Universidad Politécnica de Madrid	47,743,995 €	138
Universitat Politècnica de València	47,435,147 €	124
Universitat Autònoma de Barcelona	46,478,150 €	99

TOP-10 Catalan Organizations in Horizon 2020

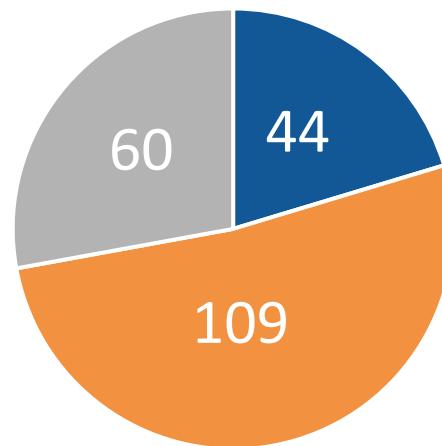
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Universitat Politècnica de Catalunya	52,595,046 €	147
ICFO	50,390,271 €	72
Universitat Autònoma de Barcelona	46,478,150 €	99
Universitat de Barcelona	43,292,105 €	120
CRG	36,333,838 €	62
LEITAT	26,988,611 €	60
EURECAT	25,713,104 €	60
IDIBAPS	17,874,593 €	35

Evolution of funding



Secured funding

Secured funding 2004-2018 (M€)



■ NATIONAL ■ EUROPE ■ COMPANY

Total: 213 M€

Data : 31st december 2018

Investments



New building

30,8 M€ (2009-2019)



MareNostrum 4

34 M€ (2016-2020)



StarLife

1,5 M€ (2016-2017)



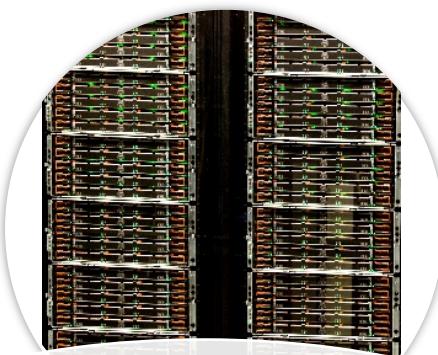
Site MareNostrum 5

14 M€ (2019-2020)



Temporary computer room

1,6 M€ (2018)

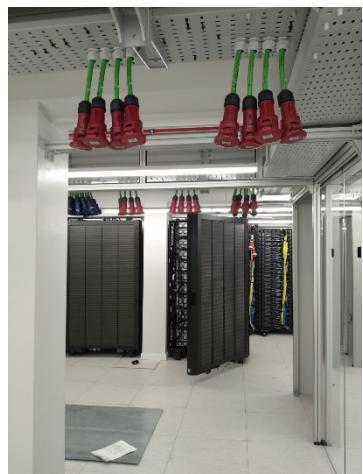


Long-term storage

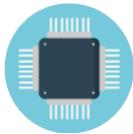
8 M€ (2019-2020)



New Computer Room



StarLife



138,2 Teraflops
of calculation power



9,5 PB of storage



9,1 Tbytes of RAM



A unique infrastructure to improve the competitiveness of Barcelona biomedical pole

New facilities in 2020

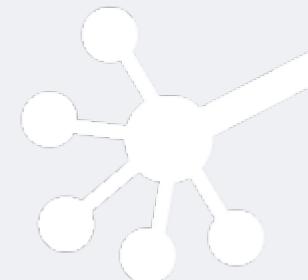


Una infraestructura abierta al público



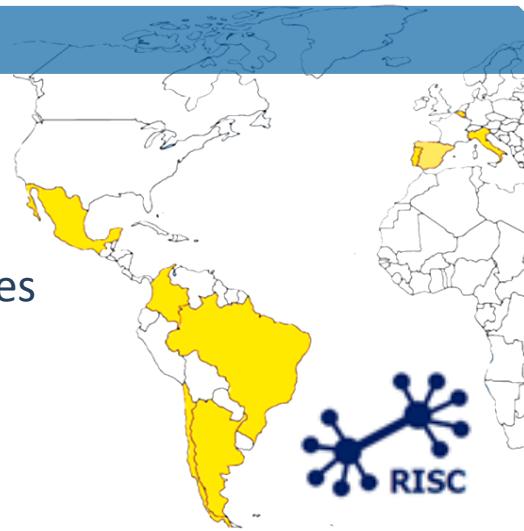
RISC Project

1. Identified research clusters for targeted research collaboration
2. Produced a Green Paper on HPC Drivers and Needs in Latin America
3. Produced a Roadmap for HPC strategic R&D in Latin America
4. Enhanced HPC R&D policy dialogue between policymakers and stakeholders



LATIN AMERICA

- Universidad Veracruzana
- Universidad de Chile
- Universidad de Buenos Aires
- Universidad Autónoma de Manizales
- Coppetec Fundação do Rio de Janeiro



EUROPE

- BSC
- CINECA
- UPM
- Menon
- Uni Coimbra

High Performance Computing for Energy

- H2020-EUB-2-2015: High Performance Computing
- 01/12/2015 - 30/11/2017
- 2 M Euro for European partners – 2M Euro for Brazilian partners



“Apply Exascale HPC technology to energy industry simulations”

Wind + Oil and Gas + Biomass

EU-Brazil OpenBio

EU-Brazil Open Data and Cloud Computing e-Infrastructure for Biodiversity

Combining **Biodiversity Science** and the **Open Access Movement** to deploy a joint European and Brazilian e-Infrastructure of open access resources supporting **the needs of the biodiversity scientific community.**



Two biodiversity use cases



Computing resources & SW platforms



Further EU-Brazil collaboration in support of the biodiversity area & infrastructures



Who benefit from EUBrazilOpenBio?

- EU & Brazilian biodiversity scientific communities
- Data and resource managers & Open Access community
- European & Brazilian policy and funding bodies



EUBrazilCC federated infrastructure



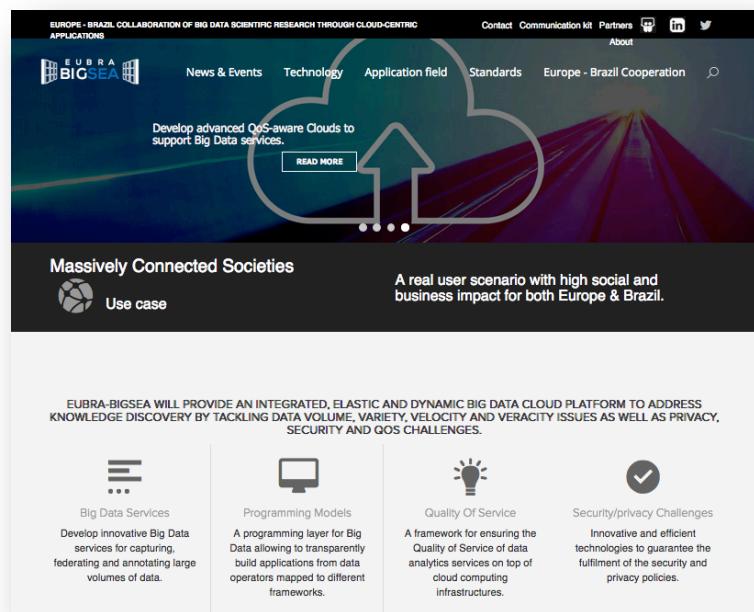
- The main objective is the creation of a **federated e-infrastructure** for research using a user-centric approach.
- To achieve this, we need to pursue three objectives:
 - **Adaptation** of existing applications to tackle new scenarios emerging from cooperation between Europe and Brazil relevant for both continents and with high social impact and innovation.
 - Integration of frameworks and **programming models** for scientific gateways and complex workflows addressing not only the requirements of the selected use cases, but a potentially much larger user community.
 - **Federation of resources**, to build up a general-purpose infrastructure comprising existing and heterogeneous resources
- Active and intense **dissemination campaign**, it will analyse innovation, and it will foster the involvement of Brazilian institutions in cloud standards definition. It will also be the first example of the **internationalisation of the EU Cloudscape series**.

EUBra-BIGSEA Project

A European-Brazilian Consortium aiming at

- The development of **QoS** and **secure cloud services** to support **Big Data**.
- The development of **Big Data services** for capturing and annotating large data
- The use of **efficient** technologies for guaranteeing the fulfilment of the **security** and **privacy** policies
- The **transfer** of this technology to a **real user scenario** with high social & business impact for both EU and BR

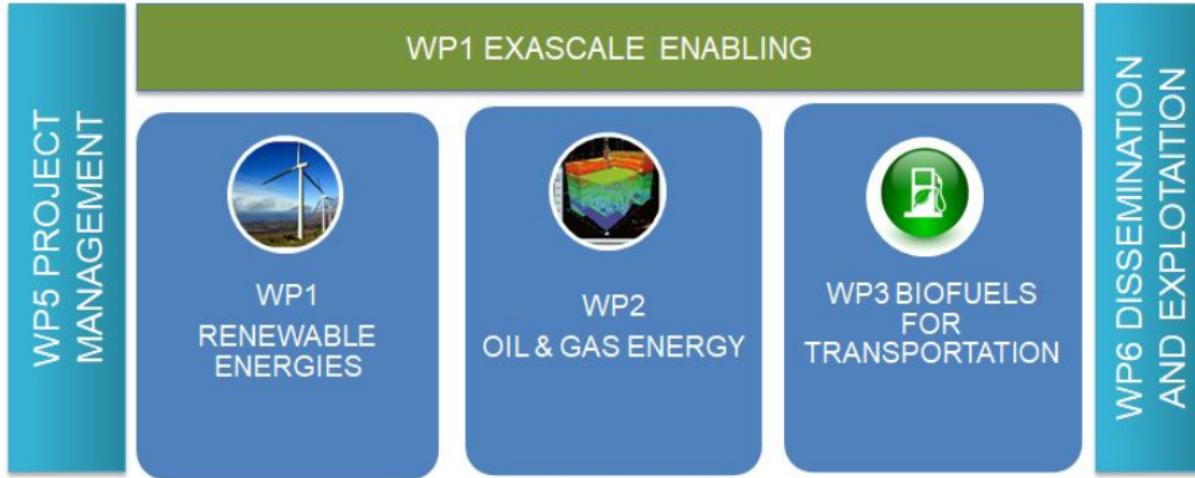
www.eubra-bigsea.eu



The screenshot shows the official website for the EUBRA-BIGSEA project. The top navigation bar includes links for Contact, Communication kit, Partners, About, News & Events, Technology, Application field, Standards, Europe - Brazil Cooperation, and a search icon. The main banner features a blue gradient background with a white cloud icon containing a gear and the text "Develop advanced QoS-aware Clouds to support Big Data services." Below the banner, there's a section titled "Massively Connected Societies" with a "Use case" icon and a description: "A real user scenario with high social and business impact for both Europe & Brazil." At the bottom, a callout box states: "EUBRA-BIGSEA WILL PROVIDE AN INTEGRATED, ELASTIC AND DYNAMIC BIG DATA CLOUD PLATFORM TO ADDRESS KNOWLEDGE DISCOVERY BY TACKLING DATA VOLUME, VARIETY, VELOCITY AND VERACITY ISSUES AS WELL AS PRIVACY, SECURITY AND QOS CHALLENGES." To the right, four boxes represent different project pillars: "Big Data Services" (developing innovative services for capturing, federating, and annotating large volumes of data), "Programming Models" (a programming layer for Big Data allowing transparent build applications from data operators mapped to different frameworks), "Quality Of Service" (a framework for ensuring Quality of Service of data analytics services on top of cloud computing infrastructures), and "Security/privacy Challenges" (innovative and efficient technologies to guarantee the fulfilment of security and privacy policies).



ENERXICO: H2020 EU-México project



2M€



2M€



**Barcelona
Supercomputing
Center**

Centro Nacional de Supercomputación

Technische
Universität
München



UNIVERSITAT
POLITÈCNICA
DE VALÈNCIA

Bull
atos technologies

UNIVERSITÉ
Grenoble
Alpes

Ciemat
Centro de Investigaciones
Energéticas, Medioambientales
y Tecnológicas



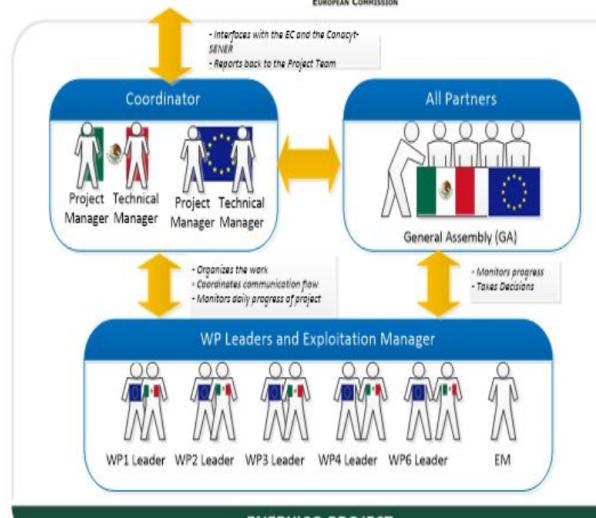
Universidad
Autónoma
Metropolitana



Instituto Politécnico Nacional



ESCUOLA SUPERIOR DE INGENIERÍA
Y ARQUITECTURA
UNIDAD ZACATECANO



BSC Academic liaison with Mexico



- BSC and CONACyT (National Council of Science and Technology) have a long-term academic liaison
 - An annual call for up-to 5 post-doc positions at BSC for Mexican researchers, of up-to 2 years each. This call will be published annually from 2016 -2020.
 - It started in 2012
 - 11 postdocs now.
 - 2017 call to be open soon.
 - **BSC agrees to not hire PhD or Post-docs from these groups as a first option.**
 - Among those 4 are already back in Mexico (ITESM, ITL, UAEMeX, UV)
 - An annual call for ten, three-year PhD scholarships for Mexican students. This call will be published annually from 2016 -2020.



Honoris Causa Universidad Veracruzana, Mexico, 2010



Honoris Causa CINVESTAV 2017





"Por su destacada contribución al desarrollo científico en México, en particular al área de la computación de alto rendimiento, el Gobierno de los Estados Unidos Mexicanos reconoció al Dr. Mateo Valero Cortés, Catedrático de la Universidad Politécnica de Cataluña y Director del Centro Nacional de Supercomputación de Barcelona, España, con la Orden Mexicana del Águila Azteca."



Medio propio de la AGE

BSC and AEMET operate the new Early Warning Advisory System for Sand and Dust Storm in Burkina Faso

30 October 2018

This system has been released by the WMO Sand and Dust Storm Warning Advisory and Assessment System (SDS-WAS).



MINISTERIO
PARA LA TRANSICIÓN ECOLÓGICA

WMO SDS-WAS N Africa-Middle East Europe RC

Burkina Faso: Expected concentration of airborne dust

Issued: 29 Oct 2018. Valid: 30 Oct 2018

AEMet
Agencia Estatal de Meteorología

RESOLUCIÓN N° 86/2018 DE ENCARGO AL BARCELONA SUPERCOMPUTING CENTER - CENTRO NACIONAL DE SUPERCOMPUTACIÓN (BSC-CNS) PARA LA REALIZACIÓN DE ACTIVIDADES DE DESARROLLO Y MEJORA DE LOS PRODUCTOS Y SERVICIOS SUMINISTRADOS POR LOS CENTROS REGIONALES DE TORMENTAS DE POLVO Y ARENA DE LA ORGANIZACIÓN METEOROLÓGICA MUNDIAL (OMM).
EXPEDIENTE: 201800005089

Título del Servicio:

RESOLUCIÓN N° 86/2018 DEL PRESIDENTE DE LA AGENCIA ESTATAL DE METEOROLOGÍA (AEMET) POR LA QUE SE ENCARGA AL "BARCELONA SUPERCOMPUTING CENTER - CENTRO NACIONAL DE SUPERCOMPUTACIÓN (BSC-CNS) EL DESARROLLO Y MEJORA DE LOS PRODUCTOS Y SERVICIOS SUMINISTRADOS POR LOS CENTROS REGIONALES DE TORMENTAS DE POLVO Y ARENA DE LA ORGANIZACIÓN METEOROLÓGICA MUNDIAL (OMM).



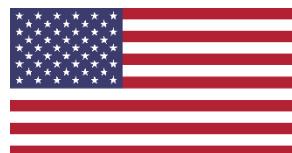
**Barcelona
Supercomputing
Center**
Centro Nacional de Supercomputación

Air Centre

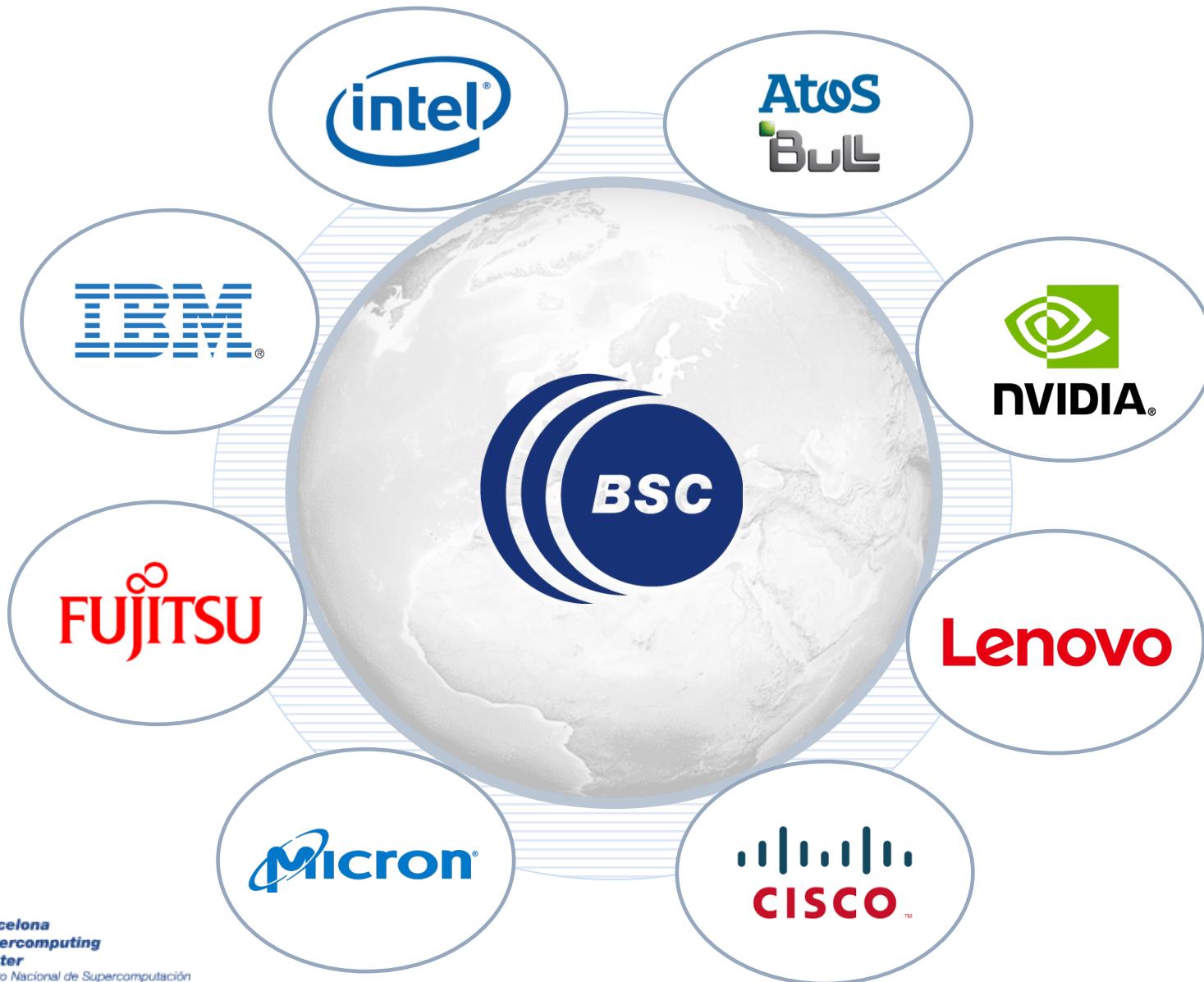


The AIR Centre is a network for international cooperation along and across the Atlantic, with an integrative approach to the advance and sharing of knowledge of space, atmosphere, oceans, climate change, energy and data science, as well as their interactions. Its central theme is North-South and South-North, East-West and West-East cooperation.

**Integrating Space, Atmosphere, Ocean, Climate, Energy
and Data Science to address the Global Challenges of the Atlantic**



BSC & The Global IT Industry 2018



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación

BSC's spin-offs in brief



NOSTRUM BIODISCOVERY, S.L.

Applies supercomputing
TO SPEED UP DRUG DISCOVERY

For the:

- **PHARMA INDUSTRY**
- **BIOTECH COMPANIES**



MITIGA SOLUTIONS, S.L.

Provides operational solutions
TO MINIMIZE THE IMPACT OF VOLCANIC ASH HAZARDS

For the:

- **AVIATION INDUSTRY**
- **ENGINE MANUFACTURES**
- **CONSULTING SECTORS**



ELEM BIO, S.L.

Provides **BIOMECHANICS SIMULATIONS**, offering software-as-a-service simulation tool focused on cardiovascular and respiratory systems

For the:

- **PHARMA INDUSTRY**
- **MEDTECH COMPANIES**
- **PUBLIC HEALTH**
- **EDUCATION**



NEARBYCOMP, S.L.

Provides **FOG COMPUTING FOR IOT**, delivering customization services for different scenarios of FOG computing

For the:

- **5G**
- **IOT**
- **SMART CITIES**

The 25 Severo Ochoa Centres



Resultados provisionales 2017

Education: Engagement with Universities

MSc in Innovation and Research in Informatics - HPC at FIB (UPC) Msc in Artificial Intelligence at FIB (UPC)

- Curriculum design
- Providing access to the BSC HPC facilities through internships
- BSC experts lecturing
- Advising Master Thesis
- Intensification on **HPC for AI** (forthcoming 2019-2020)

Double Diploma Agreement CIC(IPN-MEX)-FIB(UPC)

- Providing Master Thesis co-advising.

Doctoral Program Affiliation (main programs)

- Applied Math Program (UPC)
- Artificial Intelligence (UPC)
 - Double diploma (IPN-UPC) (forthcoming 2019-2020)
- **Computer Architecture Doctoral Program (UPC)**
 - Double diploma (IPN-UPC) (forthcoming 2019-2020)
- Environmental Engineering Doctoral Program (UPC)
- Biomedicine Doctoral Program (UB)
- Chemistry Doctoral Program(UB)

Post-Doctoral Programme CONACyT-BSC (2012-2020)

Bachelor Degree in Bioinformatics (UPF+UPC)

- Curriculum Design
- BSC experts lecturing





- Acronym for *SUPERCOMPUTING AND RELATED APPLICATIONS International Post-Doctoral program*, co-funded by BSC and the European Commission.
- STARS aims at attracting excellent postdoctoral fellows at BSC, fostering their **training in all fields of High Performance Computing and related applications**, in a stimulating, **international and interdisciplinary** environment, with the goal to boost their career perspectives as successful independent researchers

STARS

POST-DOCTORAL PROGRAM

- Total duration of the program : 5 years
- Started on 01.01.2018
- Nr of fellows to be recruited: 20 in 2 batches
- Duration of 1 fellowship: 24 months
- Eligible only if they have not spent more than 12 months of the last 36 before the deadline in Spain
- **Next call for 12 fellows opens in July 2019:**
on www.bsc.es



Summary



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación

EXCELENCIA
SEVERO
OCHOA

MOBILITY: EEBB-FPI/FPU, Jose Castillejo, Salvador Madariaga

Predoctoral Grants



Postdoctoral Grants



FUNDACIÓN
RAMÓN ARECES

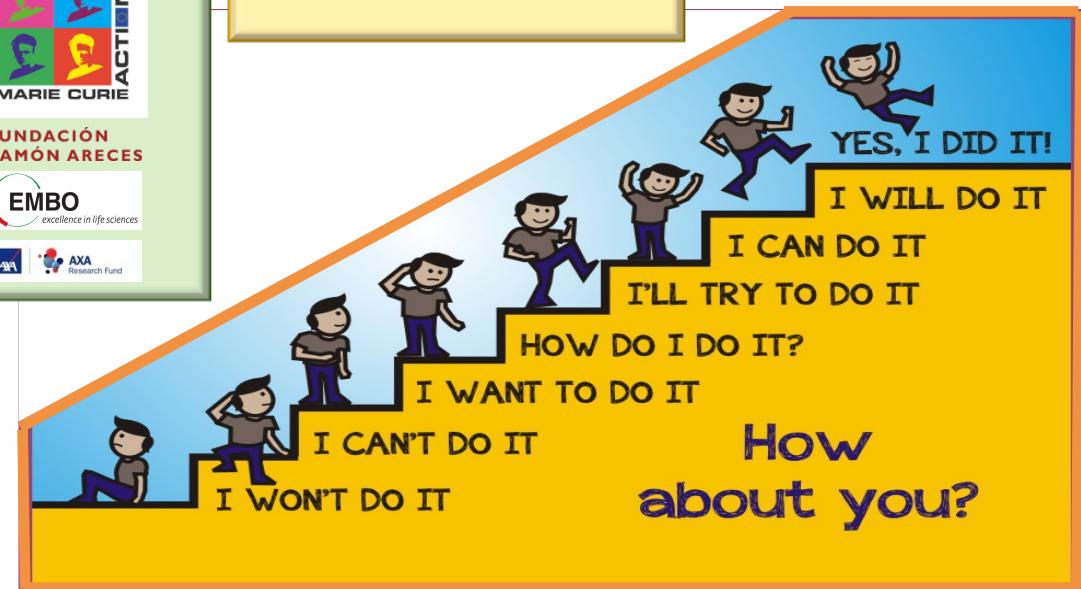


Ramón y Cajal



Programa I3

ICREA



Years of experience

Continue international collaborations: Joint Laboratory on Extreme Scale Computing



In June 2014, the University of Illinois at Urbana-Champaign, INRIA, Argonne National Laboratory, Barcelona Supercomputing Center and Jülich Supercomputing Centre formed the Joint Laboratory on Extreme Scale Computing.

The Joint Laboratory focuses on software challenges found in extreme scale high-performance computers.

Researchers from the different centers regularly meet for workshops, and in the last one, in November 2014, researchers from Riken AICS also took part.

Benchmarking

Barcelona Supercomputing Centre 2017

529

170

11.1
Petaflops

10

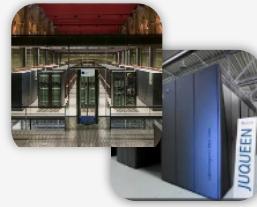
157



Staff



Articles
per year



Peak
performance



H2020 and ERC
Coordinated projects



Ongoing externally
funded projects

219

90

12
Petaflops

13

67

Jülich Supercomputing Centre 2017



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación



Proyecto “Somos Investigadoras”



Canción “MareNostrum”
a cargo del grupo
Macedònia

Visitas
“en femenino” para
alumnas de primaria
(6.000 el curso 2018-19)



Visitas guiadas por
educadoras formadas
en computación

Presencia mayoritaria
de mujeres en los
materiales educativos



Inicio 3 de octubre 2018



8 actividades lúdicas de
introducción a la
supercomputación





DM
MUDA

www.masmuda.com

93 312 0

FRAGIL

High Performance Artificial Intelligence

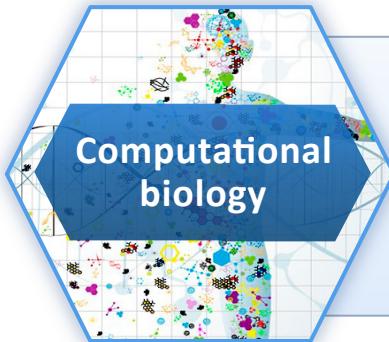
Artificial Intelligence has become a new big trend covering almost all our fields of expertise.



BSC will be very active in AI research

- **from the HPC/AI convergence** (based on a co-design vision that embraces novel algorithms, system software and architectural support, with the aim of driving the adoption of AI technologies in future simulation frameworks)
- **to its application to the priority areas of the Severo Ochoa Strategic Plan** (Health, Energy and Environment).

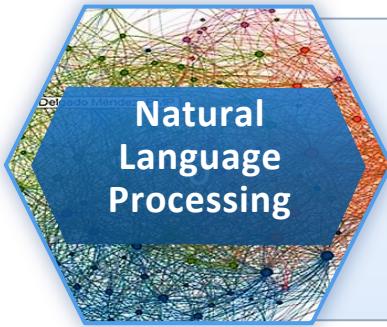
Boosting new research groups



Computational
biology



Quantic group



Natural
Language
Processing

Creating and boosting Life Sciences new research groups: Computational Biology, Integrative Computational Network Biology, Computational Regulatory Genomics and Comparative Genomics, and 4 new units



- Strategies to exploit small and medium size quantum computers
- Adapt realistic problems to quantum annealing
- Develop a quantum OS to run a small quantum device
- Build a small-sized quantum processor
- Implement quantum algorithms on it to run applications on optimization, quantum simulation and machine learning

- Projects combining Natural Language Processing (NLP), HPC and AI and Machine Learning techniques, in areas from cybersecurity to toxicology, including social impact.
- BSC to become the coordinator of the Spanish national plan for language technologies (SEAD) creating an NLP platform for health, law and public administration

The European Processor Initiative (EPI)



**Barcelona
Supercomputing
Center**

Centro Nacional de Supercomputación

La hoja de ruta internacional hacia la exaescala

2011 K computer. 10 PFlop/s

2012

2013 Tianhe-2. 33 PFlop/s.

2014

2015

2016 Sunway. 125 PFlop/s

2017

2018 Summit 200 PFlop/s. Sierra 125 PFlop/s. ABCI 0.55 EFlop/s (DL) 37 Pflop

2019

2020 Shuguang/Sugon 1 EFlop/s Tianhe-3. 1 EFlop/s

2021 Aurora 21. 1 EFlop/s >200 PFlop/s >200 PFlop/s

2022 Frontier. >1 EFlop/s Post-K computer 1 EFlop/s

2023 El Capitan >1 EFlop/s ANL system? >1 EFlop/s >1 EFlop/s >1 EFlop/s



Why Europe needs its own Processors

- Processors now control almost every aspect of our lives
- **Security** (back doors etc.)
- Possible **future restrictions on exports to EU** due to increasing protectionism
- A **competitive EU supply chain** for HPC technologies will create jobs and growth in Europe

The collage consists of four separate news snippets arranged in a diamond shape:

- Top Left:** "Amazon exec and Super Micro CEO call retraction of spy chip story". It features a small image of a computer monitor with multiple eyes on it.
- Top Right:** "NSA May Have Backdoors Built Into Intel And AMD Processors". It includes a small image of a world map with glowing lines and nodes.
- Bottom Left:** "A group of researchers showed how a Tesla Model S can be hacked and stolen in seconds using only \$600 worth of equipment". It has a small image of a Tesla Model S car.
- Bottom Right:** "Car hacking remains a very real threat as autos become ever more loaded with tech". It features a small image of a fighter jet in flight.

Overlaid on the bottom right snippet is the text: "The US Cloud Act v The EU's GDPR - Data Privacy & Security".
Below the bottom right snippet is the text: "A jet sale to Egypt is being blocked by a US regulation, and France is over it".

Images courtesy of European Processor Initiative

Where Europe needs to be stronger

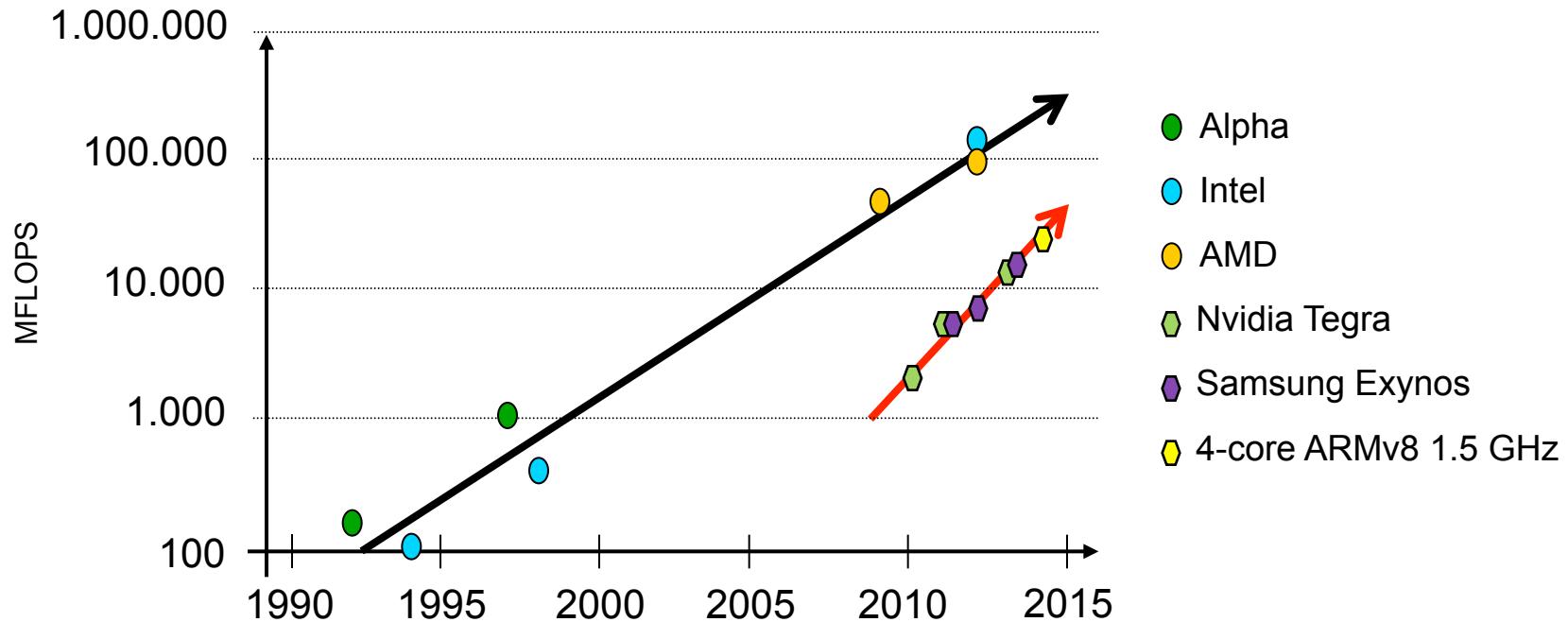
- Only 1 of the 10 most powerful HPC systems is in the EU
- HPC codes must be upgraded
- Vital HPC hardware elements are missing: General Purpose



Processor and Accelerators

EU needs its own source of as many of the system elements as possible

The Killer Mobile processors™



- « Microprocessors killed the Vector supercomputers
 - « They were not faster ...
 - « ... but they were significantly cheaper and greener

- « History may be about to repeat itself ...
 - « Mobile processor are not faster ...
 - « ... but they are significantly cheaper and greener



Network of c.2,000 European R+D experts in advanced computing: **high-performance** and **embedded** architecture and compilation

720 members, 449 affiliated members and 871 affiliated PhD students from **430 institutions** in **46 countries**.



hipec.net



HiPEAC has received funding from the European Union's Horizon2020 research and innovation programme under grant agreement number 779656.



2011
Tibidabo

ARM multicore



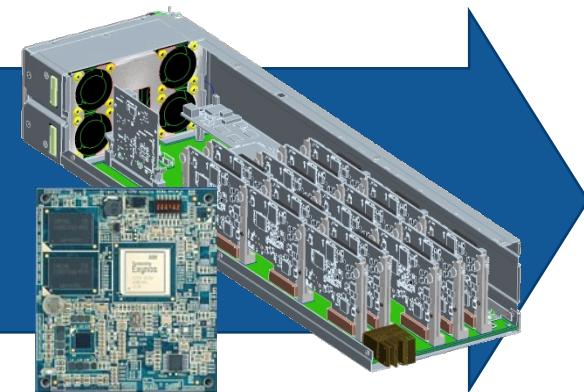
2012
KAYLA

ARM + GPU
CUDA on ARM



2013
Pedraforca

ARM + GPU
Inifinband
RDMA



2014
Mont-Blanc

Single chip ARM+GPU
OpenCL on ARM GPU



Tibidabo: The first ARM HPC multicore cluster



Q7 Tegra 2

2 x Cortex-A9 @ 1GHz
2 GFLOPS
5 Watts (?)
0.4 GFLOPS / W



Q7 carrier board

2 x Cortex-A9
2 GFLOPS
1 GbE + 100 MbE
7 Watts
0.3 GFLOPS / W



1U Rackable blade

8 nodes
16 GFLOPS
65 Watts
0.25 GFLOPS / W



2 Racks

32 blade containers
256 nodes
512 cores
9x 48-port 1GbE switch

512 GFLOPS

3.4 Kwatt
0.15 GFLOPS / W



- Proof of concept
 - It is possible to deploy a cluster of smartphone processors
- Enable software stack development

Mont-Blanc HPC Stack for ARM



Industrial applications



Applications



System software



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación



Hardware



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación



Barcelona
Supercomputing
Center
Centro Nacional de Supercomputación

Press Impacts

The Register®

HPC
Guyu

TG
DAILY

HEXUS

InformationWeek
BUSINESS INNOVATION POWERED BY TECHNOLOGY

SOFTPEDIA®
Updated one minute ago

paralelizados.com
Comunidad de usuarios de HPC

DIARIO DE CUYO

CORDIS

tve

BusinessWire
A Daykin Halliburton Company

insideHPC.

elPeriódico



lamalla.cat

EXAME.com

LAVANGUARDIA

ANANDTECH

FINANCIAL TIMES

Xabeka

WIRED
EXTREME TECH

Xbit
laboratories

HPC

COMPUTERWORLD UK
THE VOICE OF IT MANAGEMENT

CHIP

eWEEK Europe
engadget

mcPRO
MuyComputerPRO

tweakers.net

gizmología

SOCcentral

EL PUNT AVUI+



WALL STREET
JOURNAL

EE Times

euronews

COMPUTERWORLD

computing

Technology
Review

ZDNet

a
aracat

MHN
MULTIHOUSING NEWS

GOsinc

the INQUIRER
News, reviews, facts and friction

BullSequana compute blade: X1310 Marvell ThunderX2™ (ARMv8) processor



- ▶ **Atos and ARMv8**
 - Atos is the industrial pivot of MontBlanc3
 - ARM is one of the Atos strategic directions for the next years
 - Europe is leading in ARM development
 - Montblanc project is proceeding as expected
- ▶ **BullSequana X1310 blade**
 - Up to 288 nodes in one BullSequana X1000 and up to 96 nodes in one BullSequana XH2000 with:
 - 3 compute nodes with 2 Marvell ThunderX2 (ARMv8) processors
 - Up to 1024 GB of Memory per node – DDR4 @2666 MT/s (w/64GB DIMMs)
 - High-speed Ethernet, InfiniBand EDR, HDR or HDR100 on the mezzanine interconnect
 - Up to 192 cores per blade



311 TFlops



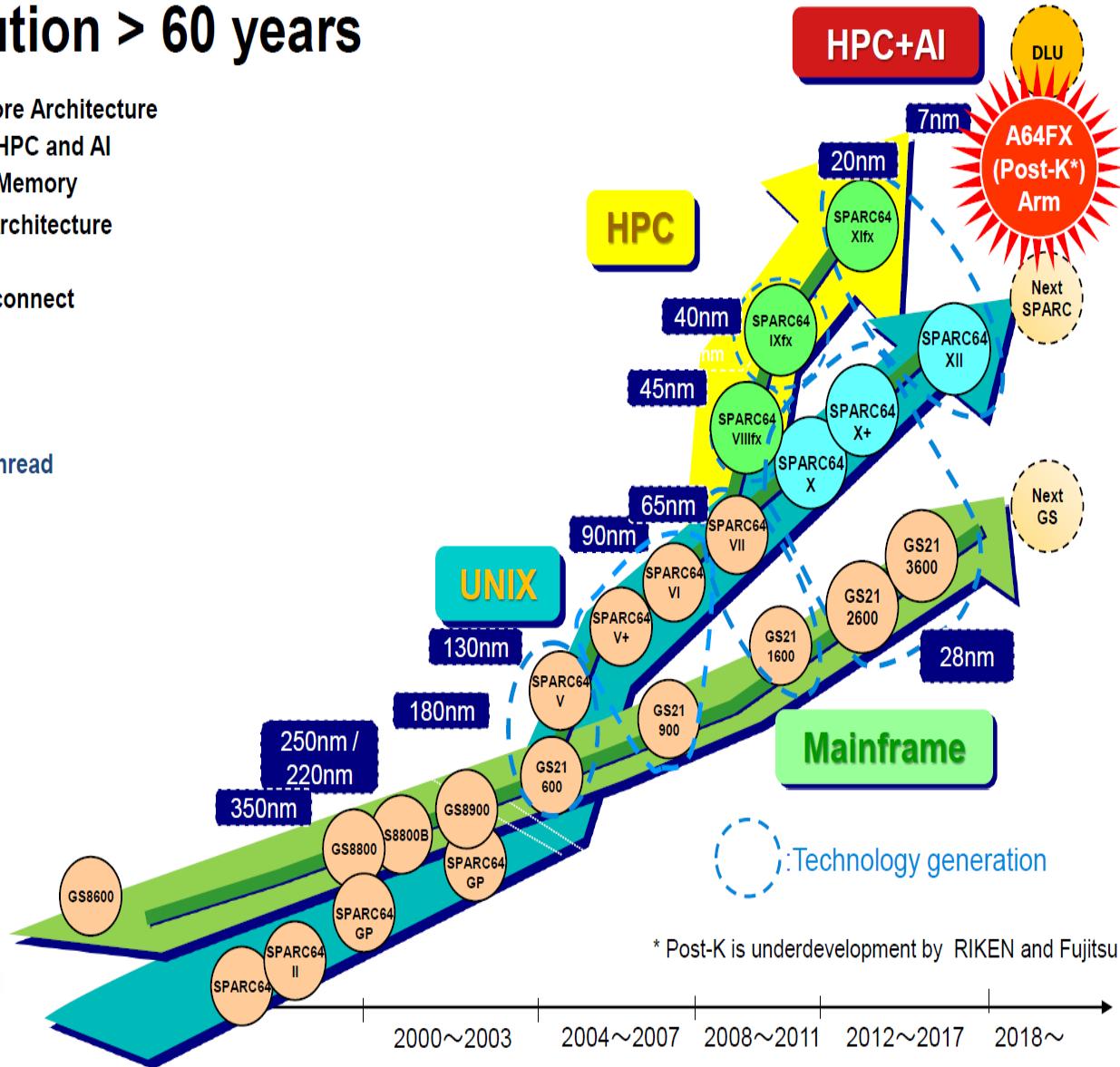
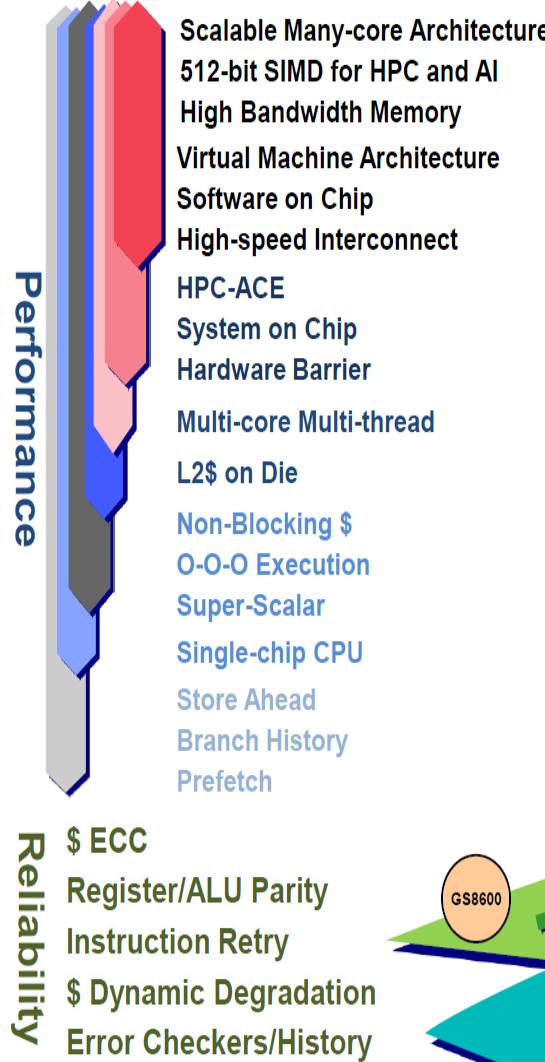
92 BullSequana X1310 blades, three compute nodes per blade, dual Marvell ThunderX2 processors of 32 cores @ 2.2 GHz, based on the Armv8-A instruction set, with 256 GB per node and Infiniband® EDR interconnect.

ARM processor – a credible alternative to X86 processor clusters

Fujitsu Processor Development

FUJITSU

Persistent Evolution > 60 years



USA

Sandia Labs:

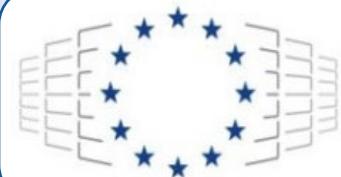
- HPE, Astra Supercomputer
- 2592 nodes, 28 core dual
- 2.3 petaflops/peak, 1.529, Linpack)
- # 203, Top500 (Nov. 2018)
- #36, HPCG (Nov. 2018)



Others: (smaller systems)

- Nercs Labs: Cray, 1080 cores
- Argonne Labs: HPE, Comanche system

European technologies & infrastructures



EuroHPC
Joint Undertaking

1 billion € 2019-2020

To build and deploy 4 world-class supercomputers for scientific and industrial users

To develop supercomputing hardware and software technologies



300 M€ until 2020

To create a single online platform where all European researchers will be able to:

- Find, access and re-use data produced by other scientists.
- Deposit, analyse and share data they have been paid to produce.

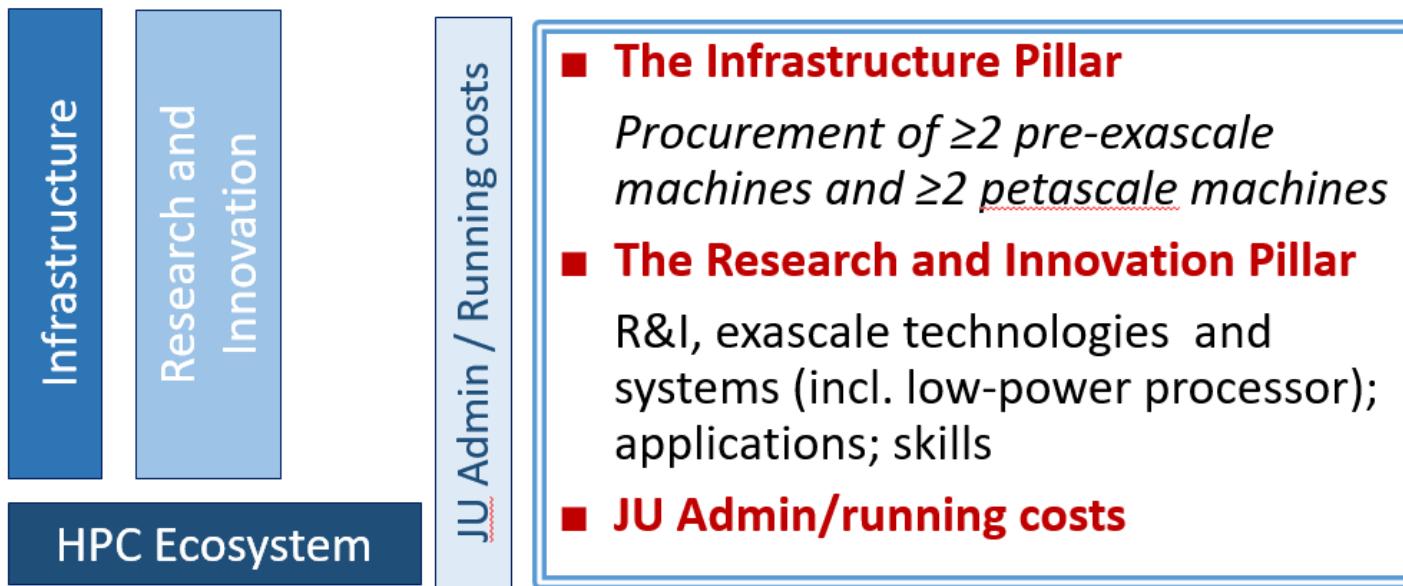
The new European framework



EuroHPC
Joint Undertaking



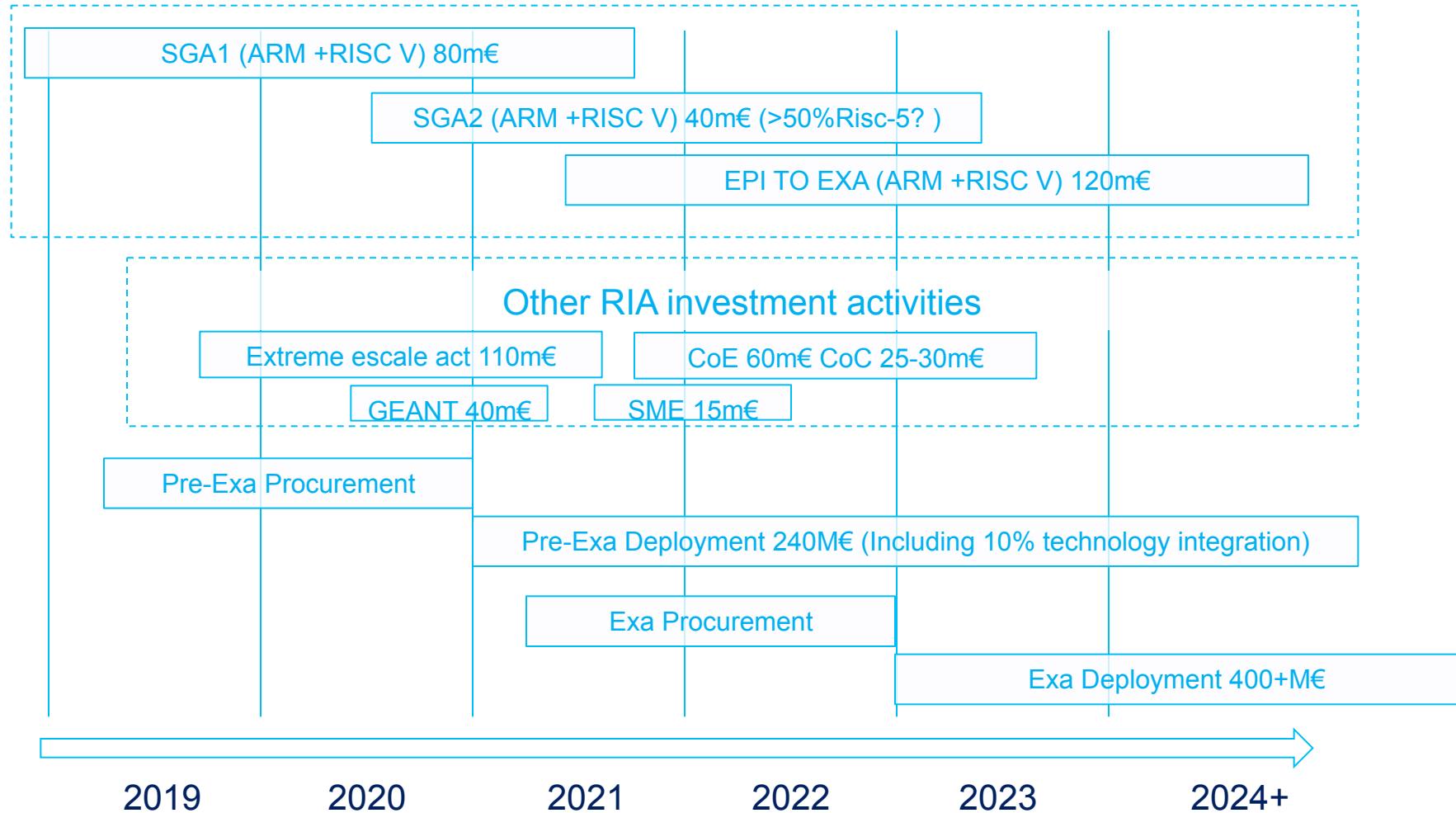
EuroHPC Activities 2019-2020



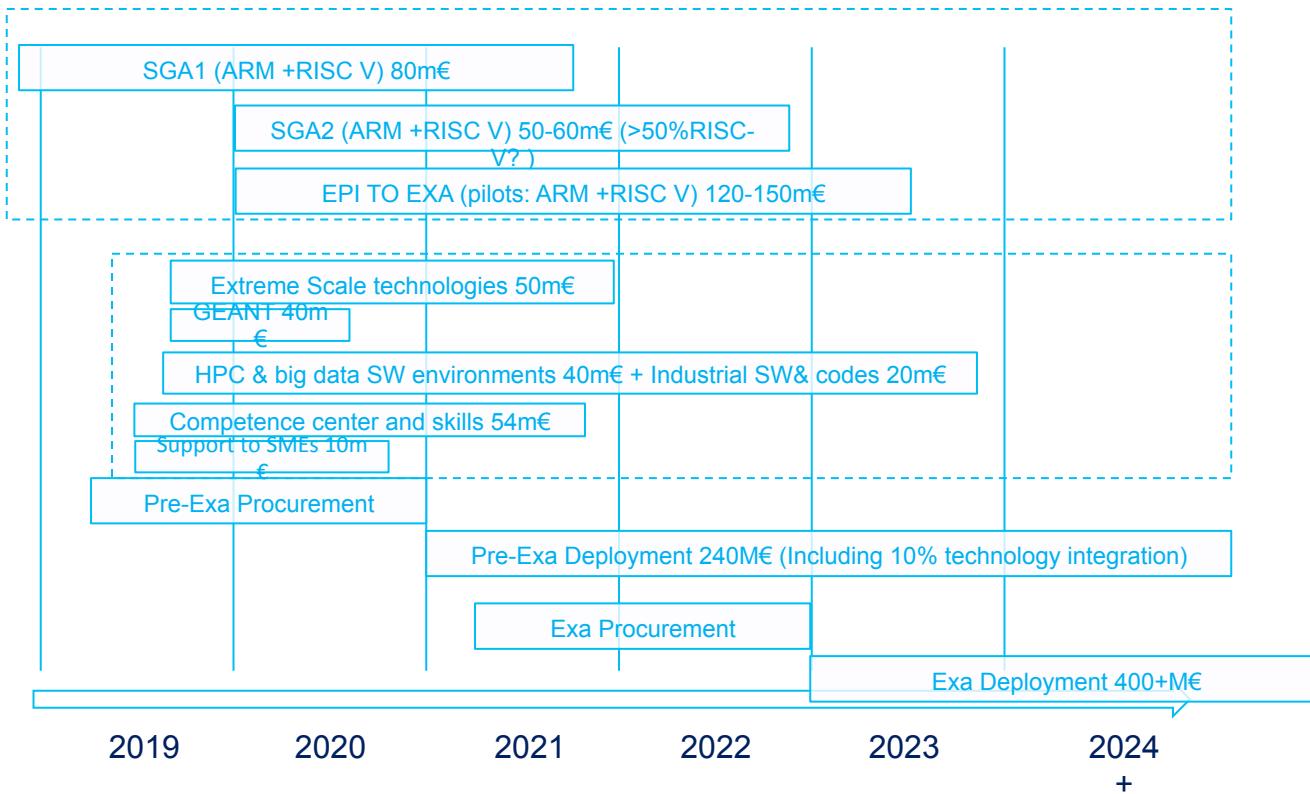
Indicative figures only! M€

~270	min 180	10	486	EC
~290	~186	10	486	Participating States (PS)
560	392	20	972	Total
0	~420 (in kind)	2	422	Private Members

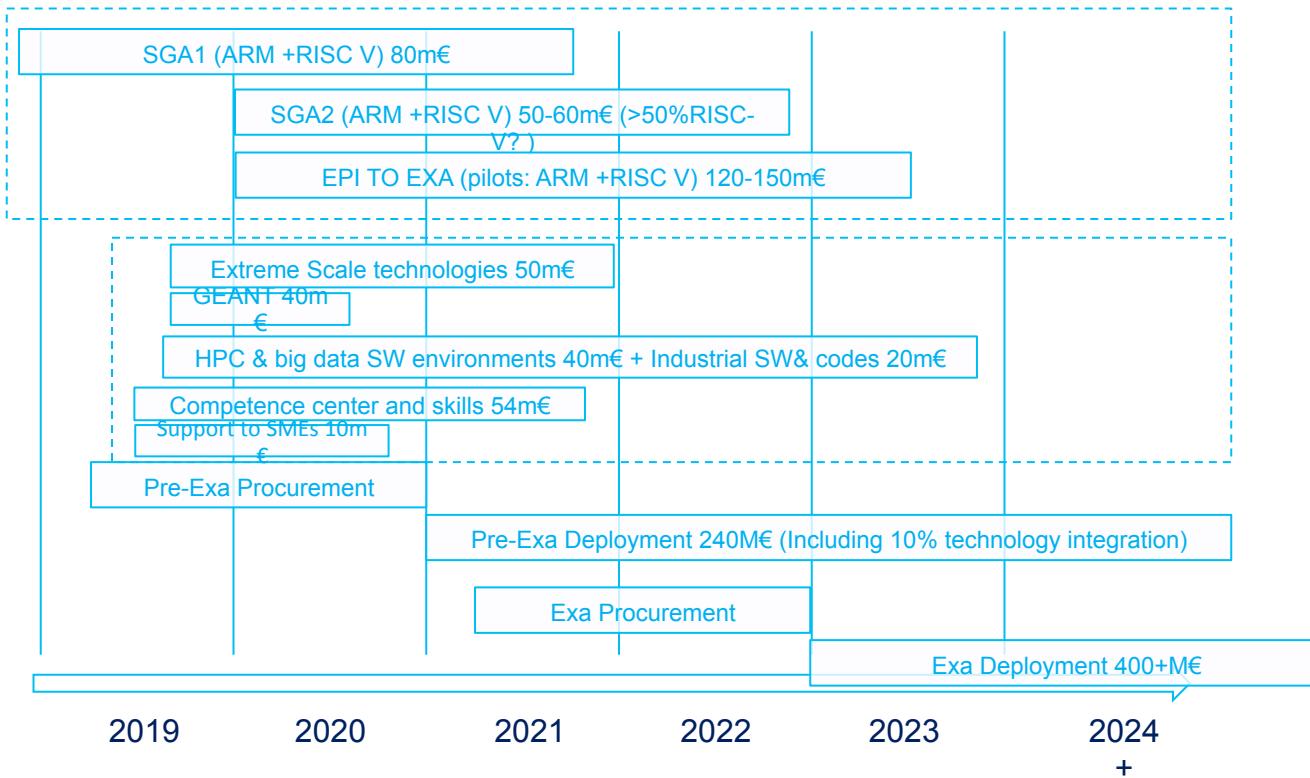
HPC Roadmap



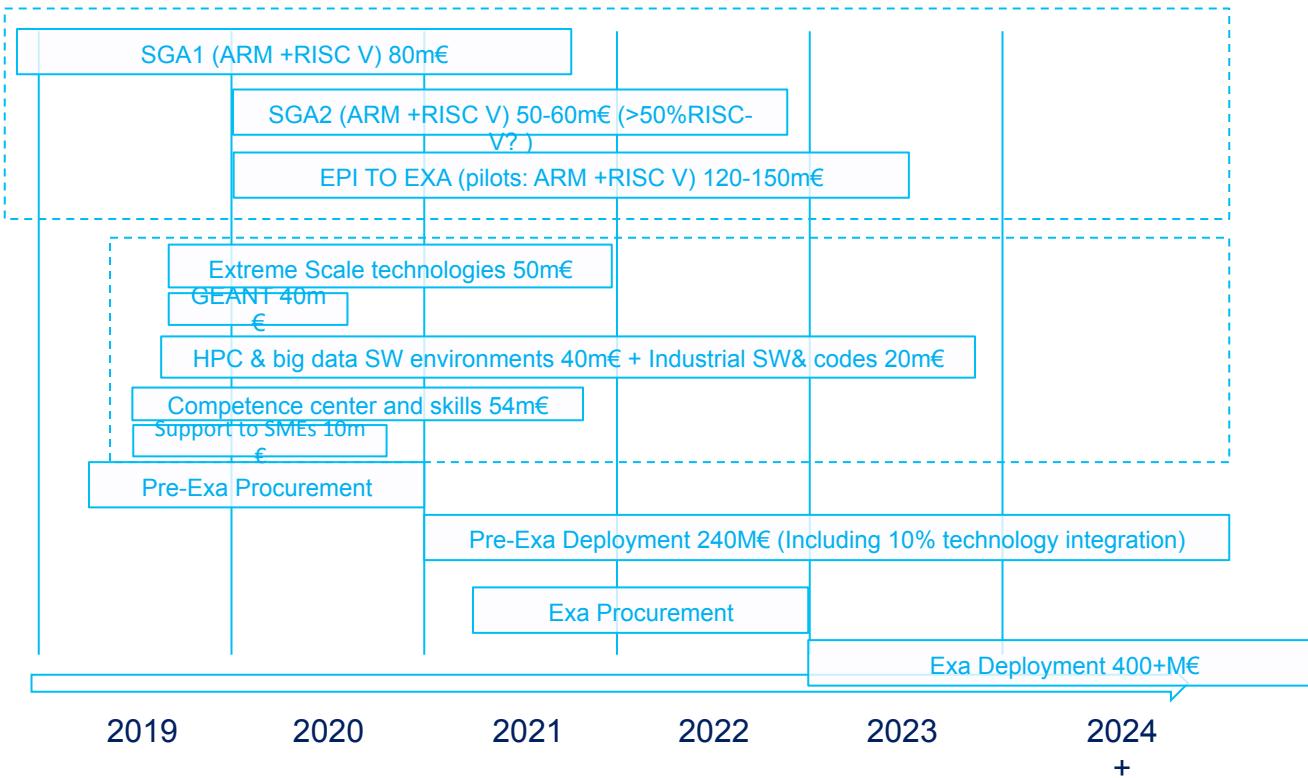
HPC Roadmap



HPC Roadmap



HPC Roadmap



How EuroHPC will help to make us stronger

- Developing a new European supercomputing ecosystem: HPC systems, network, software, applications, access through the cloud
- Making HPC resources available to public and private users, including SMEs.
- Stimulating a technology supply industry



BSC and the EC



Final plenary panel at ICT - Innovate,
Connect, Transform conference, 22
October 2015 Lisbon, Portugal.

the transformational impact of excellent science in research
and innovation

*“Europe needs to develop an entire domestic exascale stack from the processor all the way to the system and application software”,
Mateo Valero, Director of Barcelona Supercomputing Center*

Director of Barcelona Supercomputing Center, Mateo Valero, makes a pledge for developing a strong HPC ecosystem.

Published on 12/04/2016

Europe has the competence and skills to engage in the global competition towards Exascale Supercomputing. To fully benefit from the opportunities of the digital single market, Europe must strengthen the fundamental research on which digital transformation is based and build a stronger European High Performance Computing (HPC) ecosystem.

In a [guest blog post](#) on Commissioner Günther Oettinger's [website](#) Mateo Valero stresses the need for Europe to join the race towards Exascale supercomputing. According to him, there is an open window of opportunity for the High Performance Computing (HPC) development that would stimulate scientific breakthroughs and have tremendous impact on society and industry.



HPC is a global competition



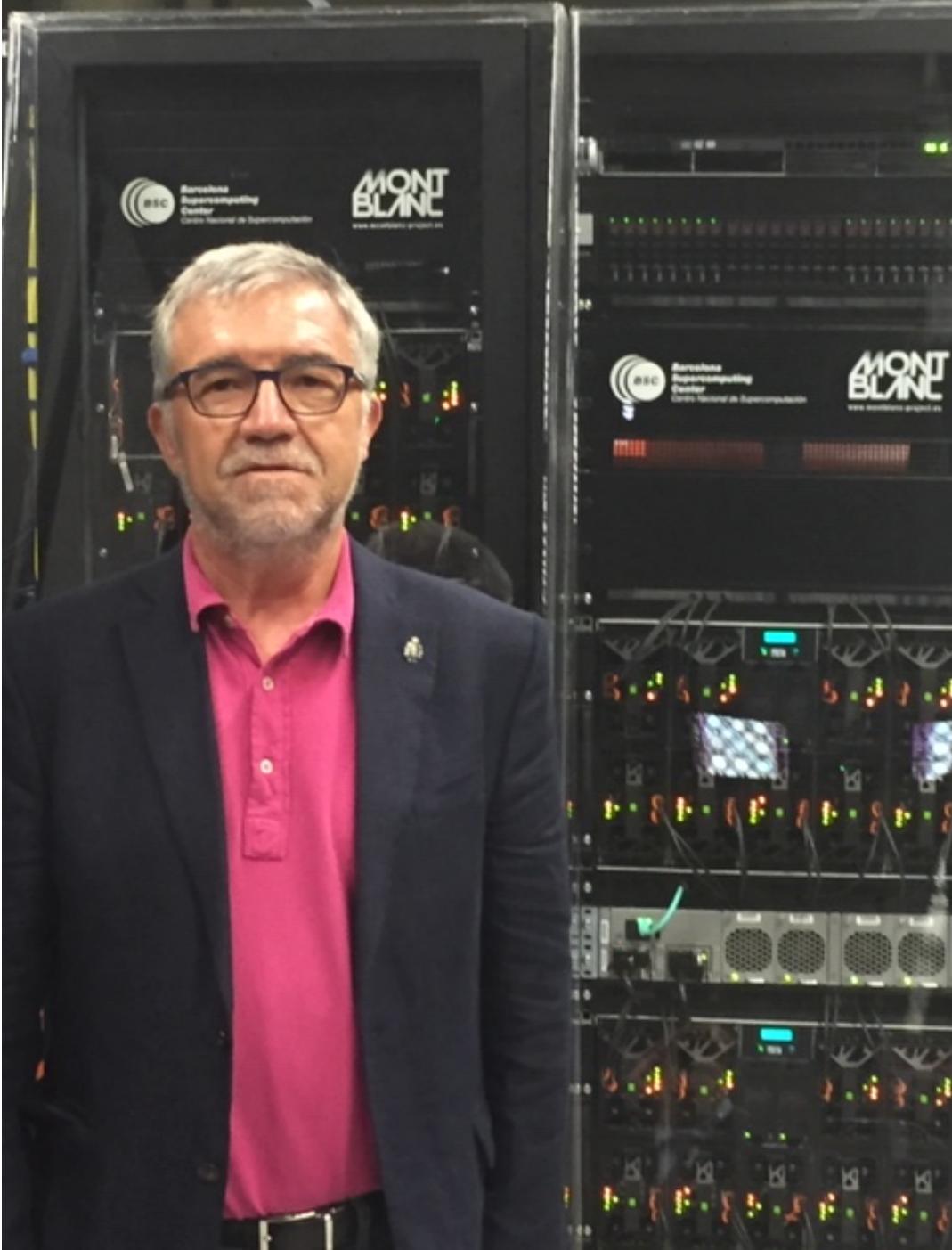
“Our ambition is for Europe to become one of the top 3 world leaders in high-performance computing by 2020”.

European Commission President
Jean-Claude Juncker (27 October 2015)

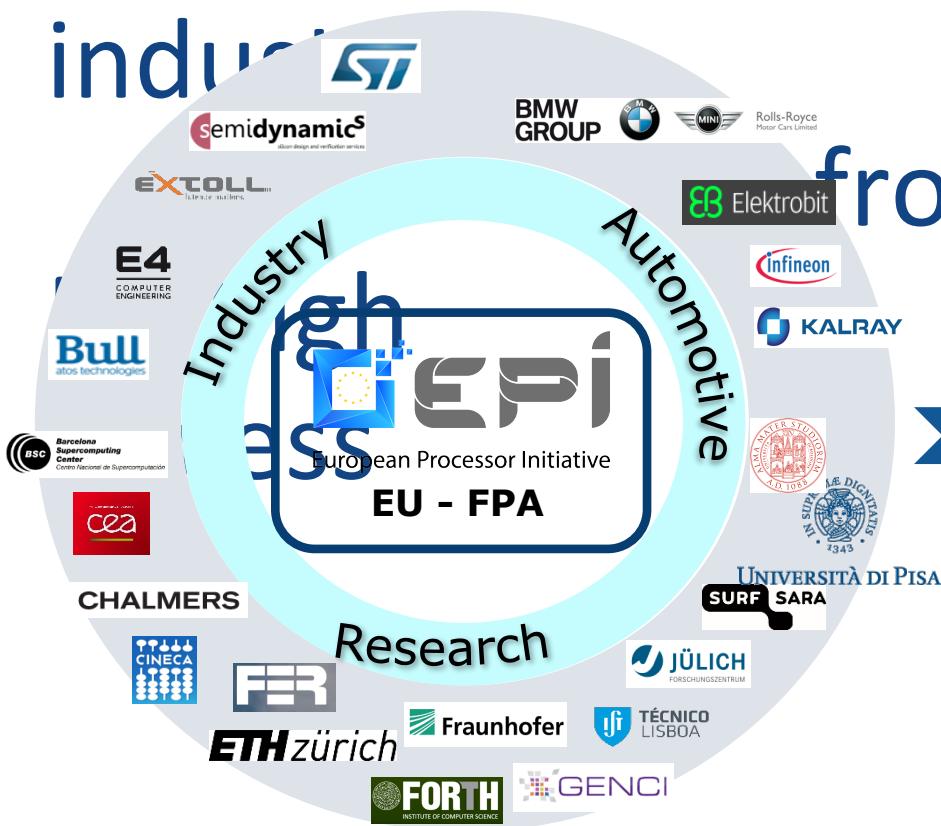
“With the EuroHPC initiative we want to give European researchers and companies world-leading supercomputer capacity”.

European Commission Vice President
Andrus Ansip (11 January 2018)





EPI 23 partners, from research to industry



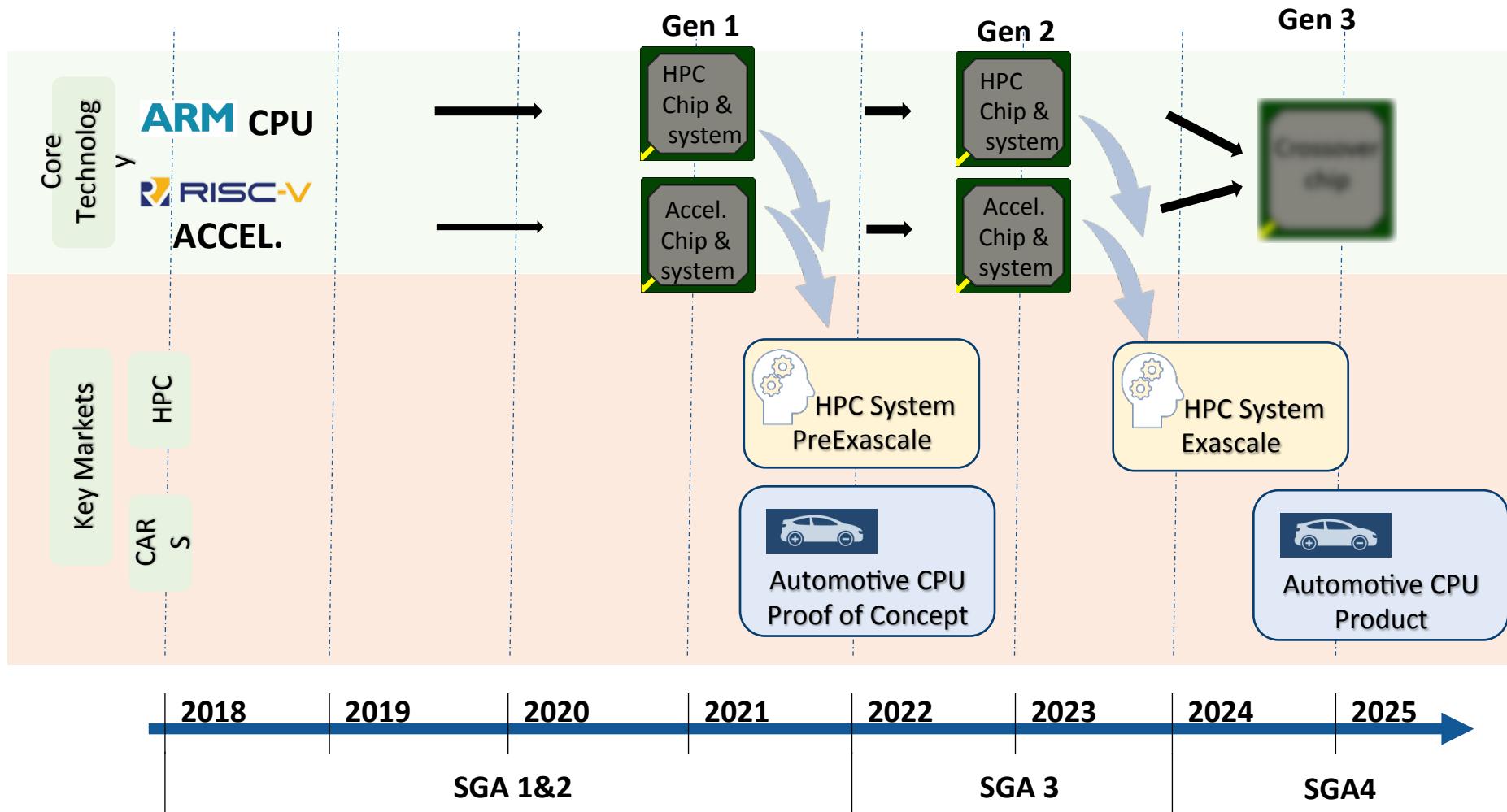
from consortium to



Fabless company
Industrial hand of EPI
Incorporated by a
couple EPI members
and external investors

1st EPI production

EPI ROADMAP



Long-term Strategic European Projects



Mare Nostrum RISC-V inauguration 202X

Por el autor de *El código Da Vinci*

DAN BROWN ORIGEN

MN-RISC-V



Planeta





**Barcelona
Supercomputing
Center**
Centro Nacional de Supercomputación



EXCELENCIA
SEVERO
OCHOA

Thank you

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10/2018